Use cases of Programmable Dataplane (P4) ECE/CS598HPN

Radhika Mittal

Which paper(s) did you read?

• (A) BeauCoup: Network Monitoring

• (B) Elmo: Multicast

• (C) Both

• (D) Neither

Network Monitoring

- Most popular usecase of programmable dataplanes.
- Lots of recent papers!
- Key challenges:
 - Dealing with small amount of memory.
 - Ensuring high line rate (small processing capability, limited memory access)
 - Supporting a wide variety of queries.

¹[bo'ku] *Adv*. many, a lot.

BeauCoup:1

Answering **many** network traffic queries,

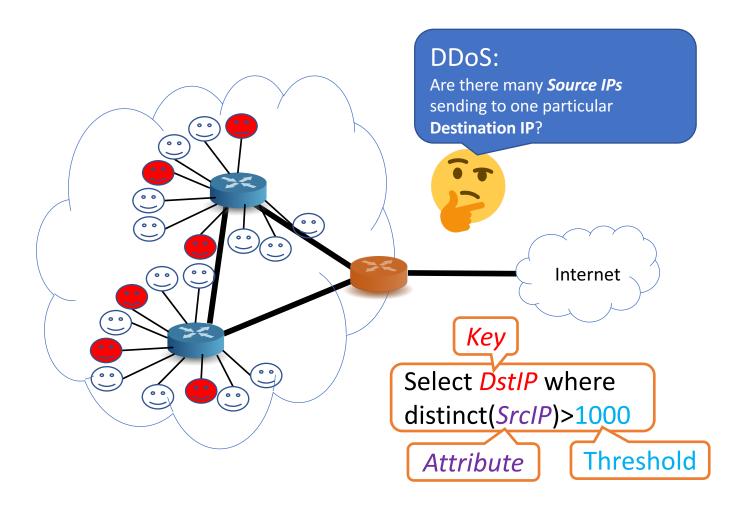
one memory update at a time!

Xiaoqi Chen, Shir Landau-Feibish, Mark Braverman, Jennifer Rexford

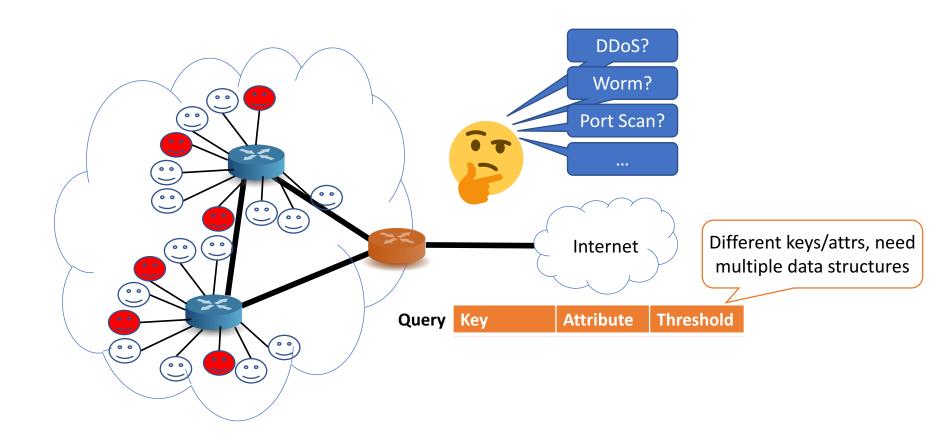




Network traffic query



Many network traffic queries



Many network traffic queries

Run 42 data structures?



I have 42 queries



Spec for today's commodity programmable switch:

- XX Tbps aggregated throughput
- W MB data-plane memory
- Can only access ZZ bytes of memory per packet

(True for CPU, FPGA, etc., as well... Moore's law!)

One memory update at a time?

- Constant memory update per packet, regardless of the number of queries?
- Game plan:
 - I. Each query uses only **o(I)** memory update per packet **on average**
 - 2. Combine many different queries, on average uses **O(I)**
 - 3. Coordinate, **at most O(I)** per packet

Today's talk

- Challenge: many queries, few memory updates
- Achieving o(I) memory access: coupon collectors
- System design: query compiler + data plane program
- Evaluation

The coupon collector problem

- 4 different coupons, collect all of them
- Random draws
- How many total draws are remined

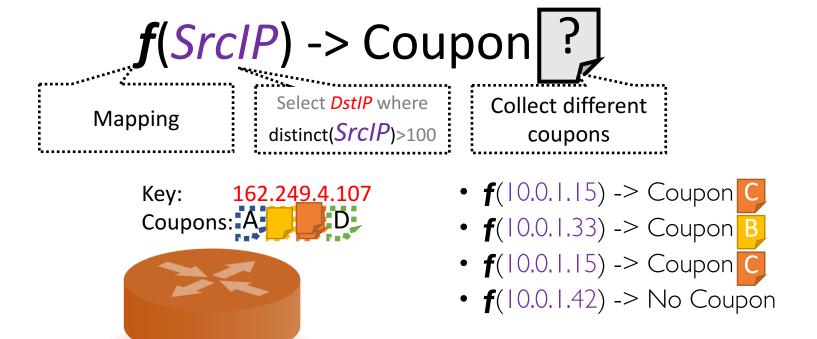


Naïve Approach

Query: Select DstIP where distinct(SrcIP)>100

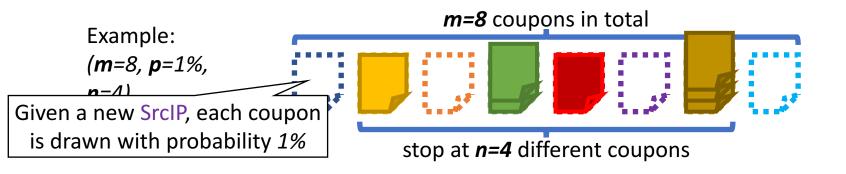
- Map each ScrIP to a coupon
 - How many total coupons?
 - How many do you need to collect?
- Issues with this approach:
 - Too much memory
 - Each packet results in a coupon collection.
 - Exceed O(1) access when multiple such queries are combined.

BeauCoup coupon collector



BeauCoup coupon collector

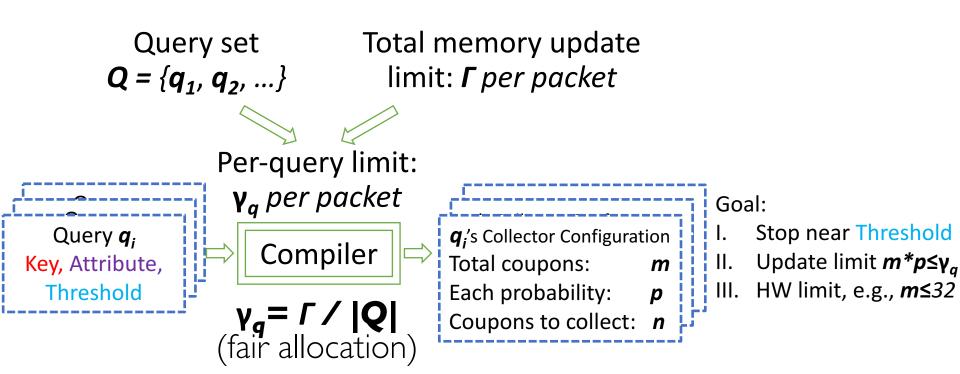
- Generalization: (**m**, **p**, **n**)-coupon collector
- m*p<1, most packets collect no coupon



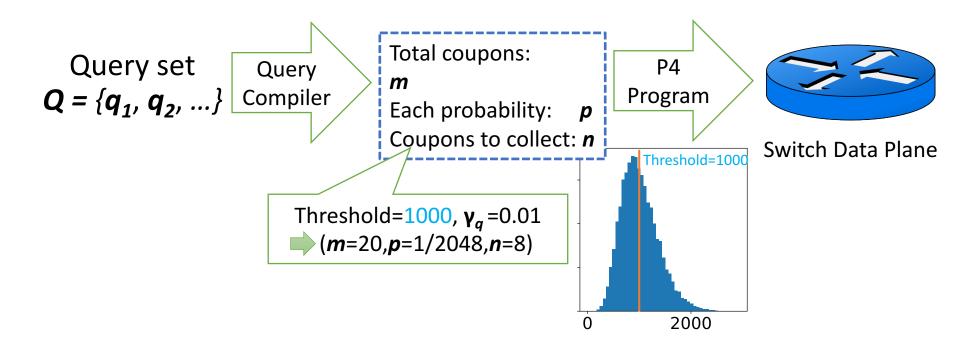
System design

- Query compiler: finds coupon collector configurations
 - Stops near query thresholds, minimize error
 - Hardware limits (e.g., memory access limit)
 - Fairness across queries
- Data plane program: collect coupons into in-memory table
 - Simultaneously run **many** queries
 - At most one coupon per packet
 - Update queries on-the-fly

Query compiler



Query compiler



Stacking queries: same attribute

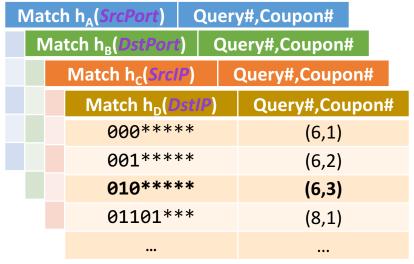
$$q_1: f(SrcIP) \rightarrow Coupon$$
 $m_1=4, p_1=1/8$
 $q_2: f(SrcIP) \rightarrow Coupon$
 $m_2=3, p_2=1/16$
 \vdots

Hash function
 $h_1(SrcIP) \rightarrow [0,1)$
 0
 $1/4$
 $1/2$
 $3/4$
 1
 $4 \text{ coupons for } q_1$
 $3 \text{ coupons for } q_2$

One hash function for each attribute

$$q_1: f(SrcIP) \rightarrow Coupon$$
 $m_1=4, p_1=1/8$
 $q_6: g(DstIP) \rightarrow Coupon$
 $m_6=3, p_6=1/8$
 $h_1(SrcIP) \rightarrow 0$
 $1/4$
 $1/2$
 $3/4$
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TCAM for selecting a coupon



Packet
SrcPort: 25012
DstPort: 443
SrcIP: 10.0.1.15
DstIP:

 $h_A(SrcPort)$ No coupon

... No coupon $h_B(DstPort)$ No coupon

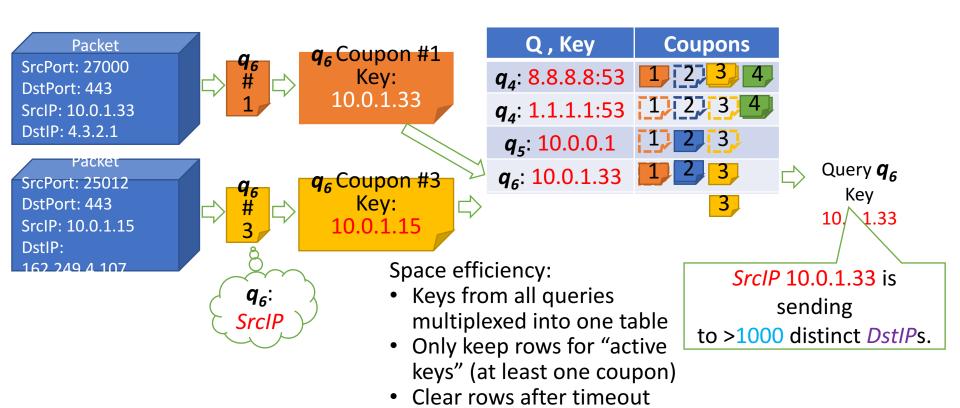
... \sharp Collect coupon (\mathfrak{q}_6 , #3) $h_A(SrcIP)=1010111$

Random tiebreak if >1 coupons

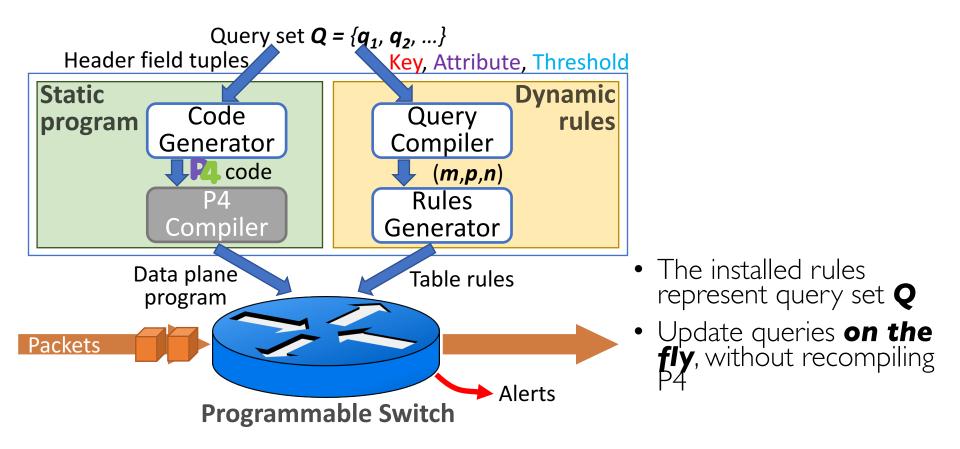
$$h_c(SrcIP) = 1010111...$$

 $h_D(DstIP) = 0101011...$

Coupon collector table



Installing queries into switches



Evaluation highlights

How efficient is BeauCoup?

We uses **4x~I0x fewer memory access** than the state-of-the-art to achieve the same accuracy.

How much hardware resource?

On the Barefoot Tofino programmable switch, BeauCoup occupies <50% of each resource

BeauCoup: Answering many network traffic queries, one memory update at a time!

- **Scalable**: built upon coupon collectors, runs many queries simultaneously
- **Versatile**: change queries on the fly, without recompiling P4 program
- **Efficient**: achieve the same accuracy using 4x-10x fewer memory accesses

Is this a good usecase of programmable dataplanes?

What are the limitations?

Your opinions

Pros

- The idea of using coupon collector problem
- Ability to limit memory usage and memory accesses, while maintaining relatively high accuracy.
- Thorough evaluation.

Your opinions

- Cons
 - Is distinct counts sufficient?
 - Can we do this at the endhosts?
 - Is fair allocation across queries the right strategy?
 - Fig 8 shows that accuracy for aggregated queries may not increase much with increased memory access.
 - Overhead of configuration?

Your opinions

- Ideas
 - Supporting a broader range of queries
 - Coordination across switches
 - Generate adversarial workloads
 - Can we achieve better accuracy?

Elmo: Source Routed Multicast for Public Clouds

Muhammad Shahbaz

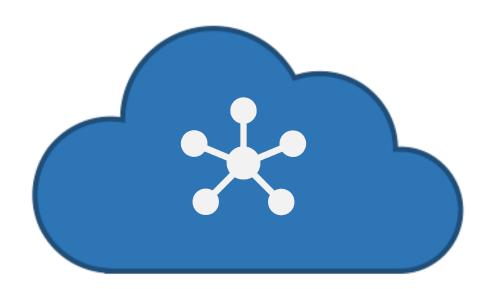
Lalith Suresh, Jennifer Rexford, Nick Feamster, Ori Rottenstreich, and Mukesh Hira

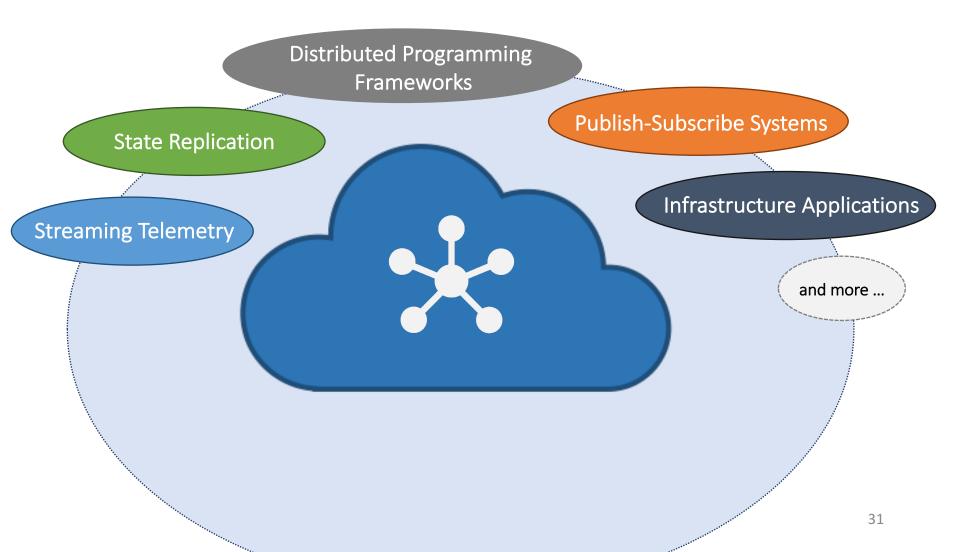


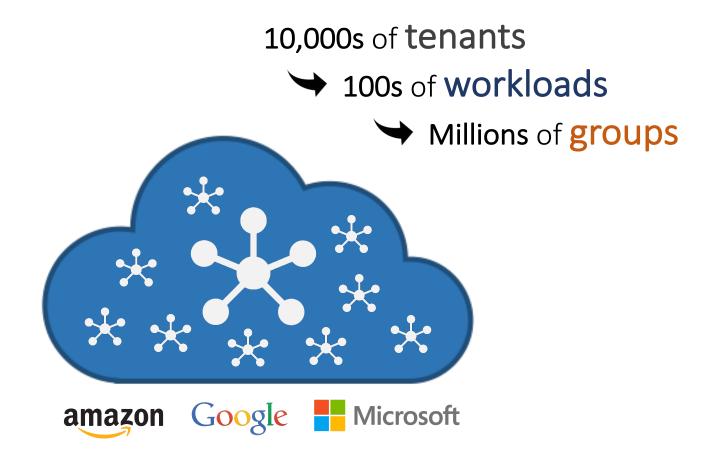


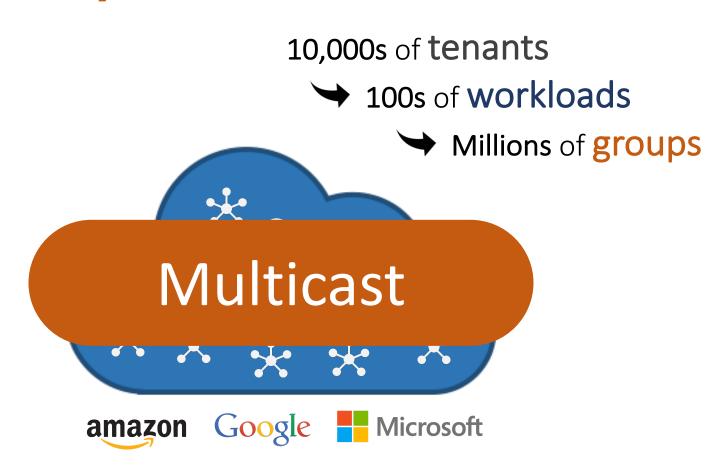


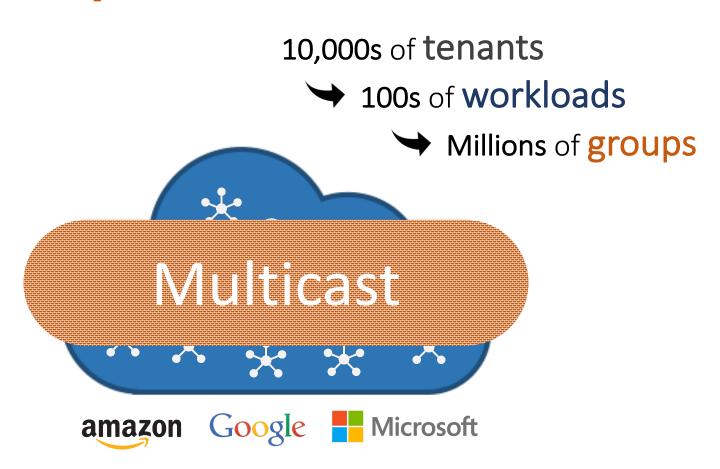






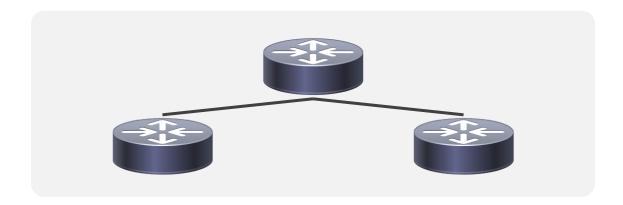




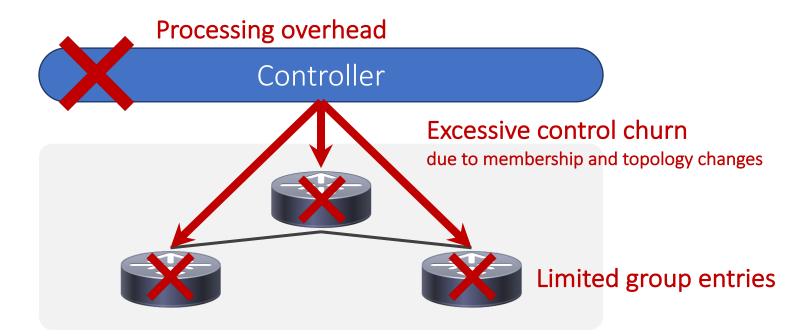


Limitations of Native Multicast

Controller

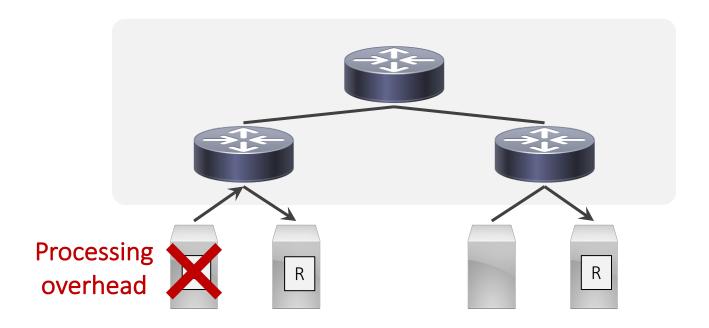


Limitations of Native Multicast



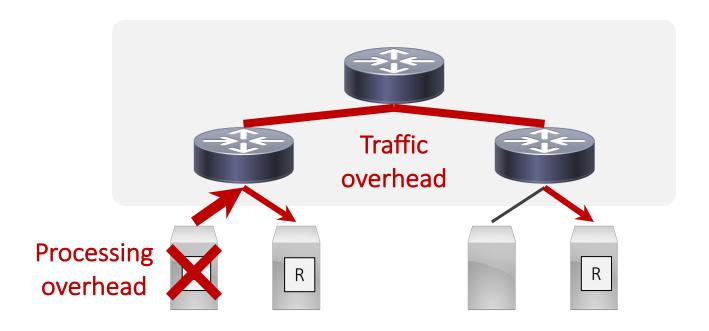
Restricted to Unicast-based Alternatives

Controller

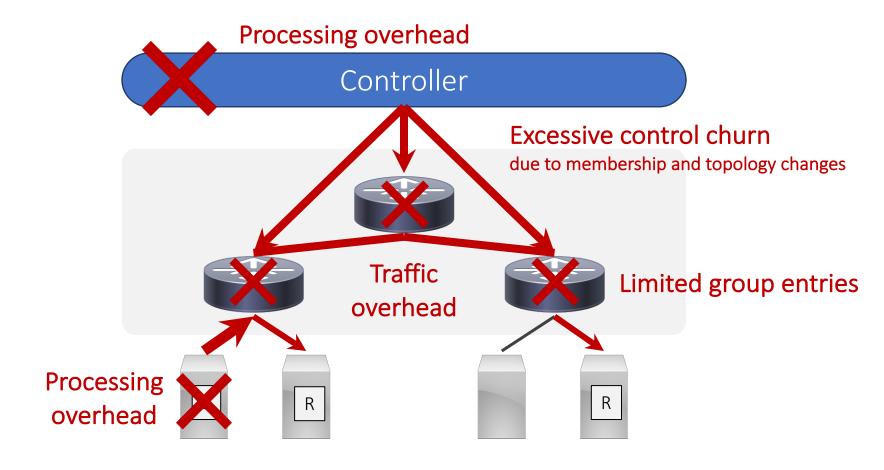


Restricted to Unicast-based Alternatives

Controller



I-to-Many Communication in the Cloud



I-to-Many Communication in the Cloud

Processing overhead

Controller

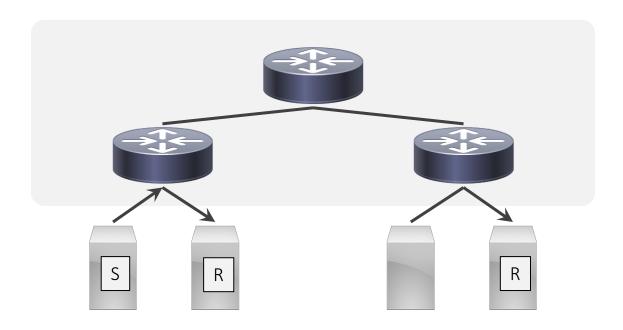
Need a scheme that <u>scales</u> to millions of groups without

excessive control, end-host CPU, and traffic overheads!



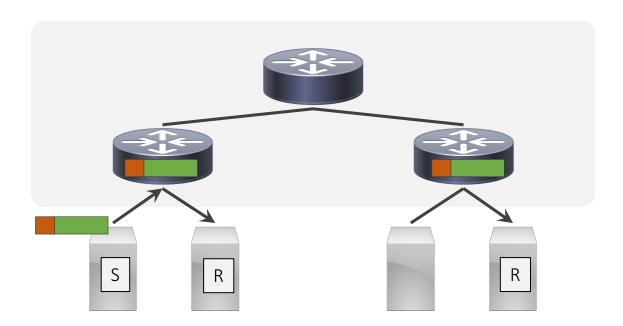
Proposal: Source Routed Multicast

Controller

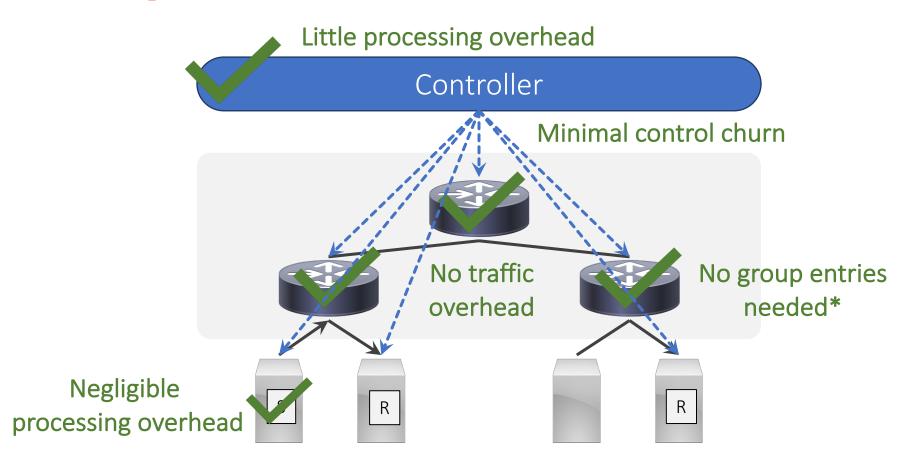


Proposal: Source Routed Multicast

Controller



Proposal: Source Routed Multicast



A Naïve Source Routed Multicast

A multicast group encoded as a list of (Switch, Ports) pairs

```
      Switch 1: [Ports]

      Switch 2: [.....]

      Switch 3: [.....]

      Switch 4: [....x..]

      Switch 5: [.x...]
```

For a data center with:

- 1000 switches
- 48 ports per switch



O(30) bytes per switch



O(30,000) bytes header

for a group spanning 1000 switches

20x the packet size!

Enabling Source Routed Multicast in Public Clouds

Key attributes:

- Efficiently encode multicast forwarding policy inside packets
- **Process** this encoding at **hardware speed** in the switches
- Execute tenants' applications without modification

A multicast group encoded as a list of (Switch, Ports) pairs

```
      Switch 1: [Ports ]

      Switch 2: [... ... ...]

      Switch 3: [... ... ...]

      Switch 4: [... ... .x ...]

      Switch 5: [.x ... ...]
```

A multicast group encoded as a list of (Switch, Ports) pairs

```
Switch 1: [Bitmap] ←

Switch 2: [.....]

Switch 3: [.....]

Switch 4: [....x..]

Switch 5: [.x....]
```

1 Encode switch ports as a bitmap

Bitmap is the internal **data structure** that switches use for **replicating packets**

A multicast group encoded as a list of (Switch, Ports) pairs

```
Switch 1: [Bitmap]
Switch 2: [.. .. ..]
Switch 3: [.. .. ..]
Switch 4: [.. .. .x ..]
Switch 5: [.x .. ..]
```

2 Group switches into layers

A multicast group encoded as a list of (Switch, Ports) pairs

Switch 1: [Bitmap]

Switch 2: [.....]

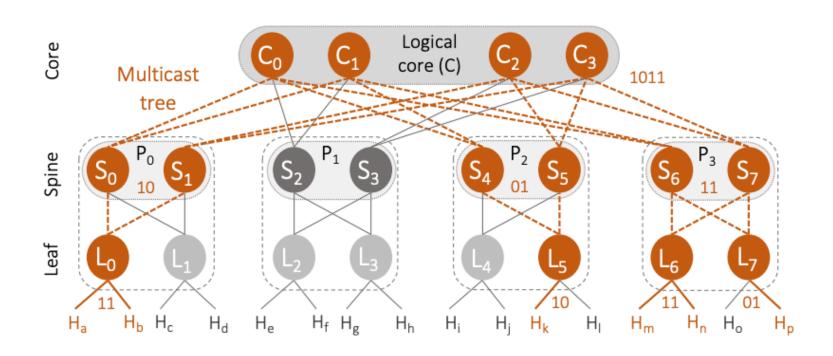
Switch 3: [.....]

Switch 4: [....x...]

Leaf

Switch 5: [.x....]

More precisely: upstream leaf, upstream spine, core, downstream spine, downstream leaf



A multicast group encoded as a list of (Switch, Ports) pairs

```
      Switch 1: [Bitmap]

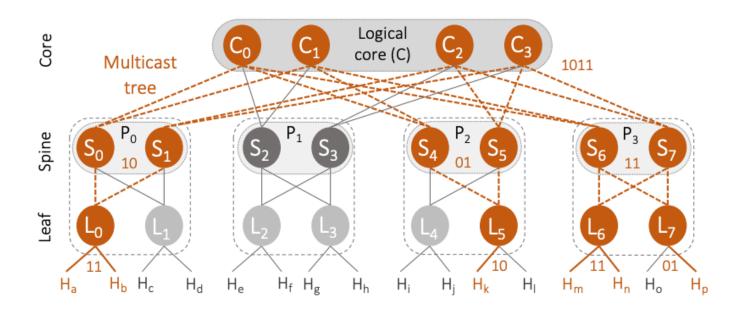
      Switch 2: [... ...]

      Switch 3: [... ...]

      Switch 4: [... ... x ...]

      Switch 5: [.x ... ...]
```

3 Switches within a layer with same ports share a bitmap



		Sender-specifi	c leaf, spine, and co	ore <i>p</i> -rules	Common downstream spine and leaf p-rules		ı
Sender H _a	type	<i>u</i> -leaf	<i>u</i> -spine	d-core	d anto-	-1 lE	
Outer header(s) VXLAN	и	01 M	00 M	0011	<i>d</i> -spine	d-leaf	Packet body
Sender H _k		At L_0 : forward to H_b and multipath to P_0	0 1	C: forward to P ₂ , P ₃	10:[P ₀]	11:[L ₀ ,L ₆] 01:[L ₇] Default	
Outer header(s) VXLAN	и	00 M	00 M	1001	D. famurand to I	L ₀ : forward to H _a , H _b	Packet body
		At L ₅ : multipath to P ₂	P ₂ : multipath to C	C: forward to P ₀ , P ₃	P_0 : forward to L_0 P_2 : forward to L_5 P_3 : forward to L_6 , L_7	L ₅ : forward to H _k L ₆ : forward to H _m , H _n L ₇ : forward to H _p	

A multicast group encoded as a list of (Switch, Ports) pairs

Switch 1: [Bitmap]

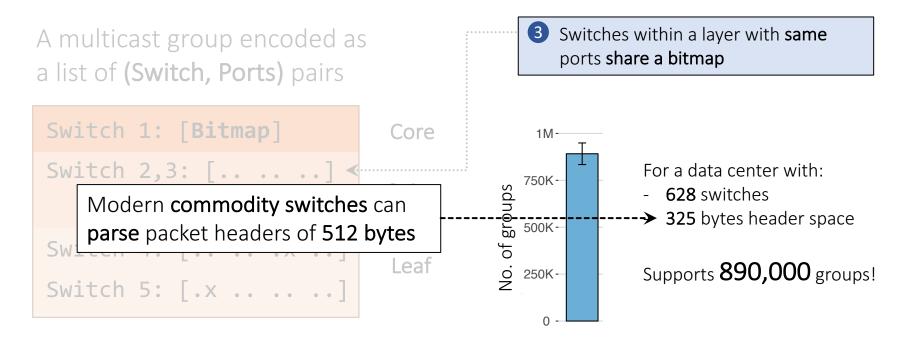
Switch 2,3: [.....]

Spine

Switch 4: [....x...]

Switch 5: [.x....]

Leaf



Core

Spine

Leaf

A multicast group encoded as a list of (Switch, Ports) pairs

```
Switch 1: [Bitmap]
Switch 2,3: [.....]

Switch 4: [....x ...]
Switch 5: [.x .....]
```

3 Switches within a layer with same ports share a bitmap

Core

Spine

Leaf

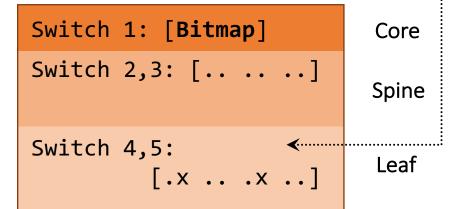
A multicast group encoded as a list of (Switch, Ports) pairs

```
Switch 1: [Bitmap]
Switch 2,3: [.....]

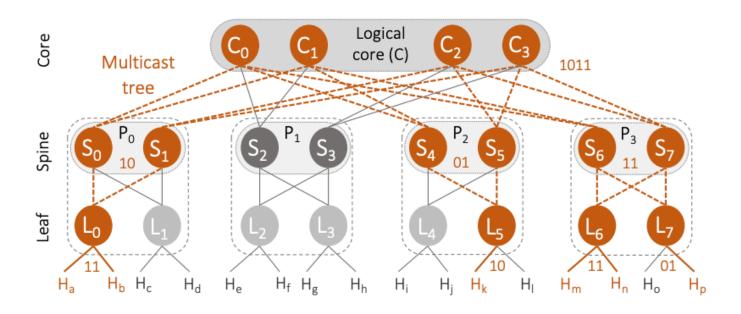
Switch 4: [....x...]
Switch 5: [.x....]
```

4 Switches within a layer with N different ports share a bitmap

A multicast group encoded as a list of (Switch, Ports) pairs



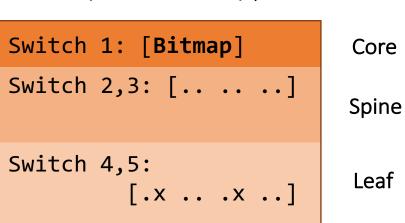
4 Switches within a layer with N different ports share a bitmap



		Sender-specifi	c leaf, spine, and c	ore <i>p</i> -rules	Common downstream spine and leaf p-rules		ı
Sender H _a	type	<i>u</i> -leaf	<i>u</i> -spine	d-core	d anto-	-1 l£	
Outer header(s) VXLAN	и	01 M	00 M	0011	<i>d</i> -spine	d-leaf	Packet body
Sender H _k		At L_0 : forward to H_b and multipath to P_0	0 1	C: forward to P ₂ , P ₃	10:[P ₀]	11:[L ₀ ,L ₆] 01:[L ₇] Default	
Outer header(s) VXLAN	и	00 M	00 M	1001	D. famurand to I	L ₀ : forward to H _a , H _b	Packet body
		At L ₅ : multipath to P ₂	P ₂ : multipath to C	C: forward to P ₀ , P ₃	P_0 : forward to L_0 P_2 : forward to L_5 P_3 : forward to L_6 , L_7	L ₅ : forward to H _k L ₆ : forward to H _m , H _n L ₇ : forward to H _p	

Switches within a layer with N A multicast group encoded as different ports share a bitmap a list of (Switch, Ports) pairs Switch 1: [Bitmap] Core 1M-For a data center with: Switch 2,3: [.. ...] 750K-628 switches No. of groups Spine 325 bytes header space 500K-Switch 4,5: Supports **980,000** groups! Leaf [.x .. .x ..] 250K-0 ó 6 Difference in ports

A multicast group encoded as a list of (Switch, Ports) pairs



4 Switches within a layer with N different ports share a bitmap

Core

Spine

Leaf

A multicast group encoded as a list of (Switch, Ports) pairs

Default Bitmap
Switch Table Entries

Use switch entries and a default bitmap for larger groups

Core

Spine

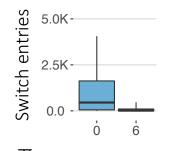
Leaf

A multicast group encoded as a list of (Switch, Ports) pairs

Default Bitmap

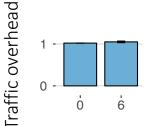
Switch Table Entries

Use switch entries and a default bitmap for larger groups



For a data center with:

- **628** switches
- **325** bytes header space



Difference in ports

Core

Spine

Leaf

A multicast group encoded as a list of (Switch, Ports) pairs

Default Bitmap
Switch Table Entries

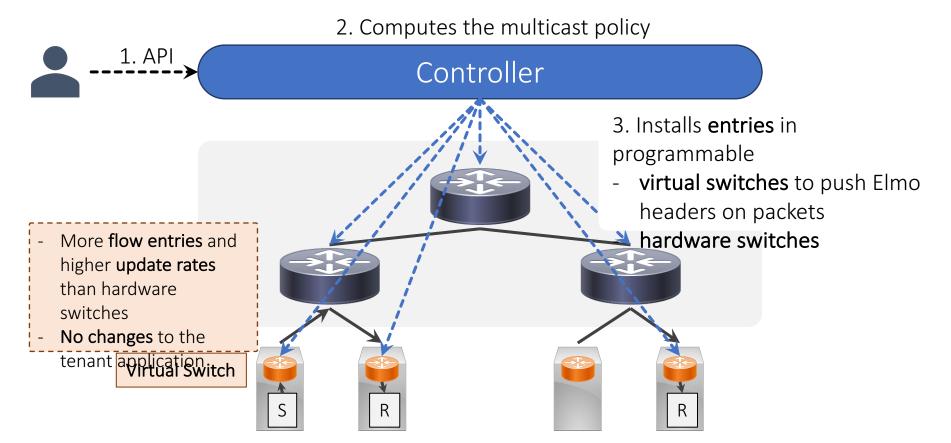
- 1 Encode switch ports as a bitmap
- 2 Group switches into layers
 - Switches within a layer with:
 - same ports share a bitmap
- 4 N different ports share a bitmap
- Use switch entries and a default bitmap for larger groups

For a data center with:

- **628** switches
- **325** bytes header space

Supports a Million groups!

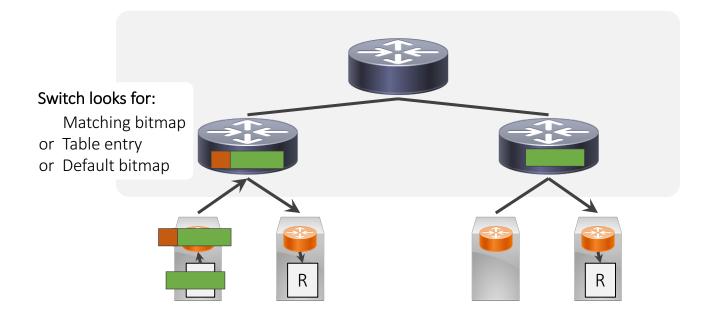
Processing a Multicast Policy in Elmo



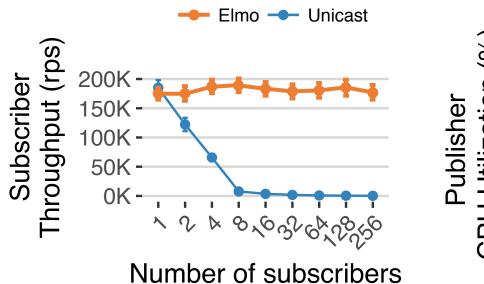
Processing a Multicast Policy in Elmo

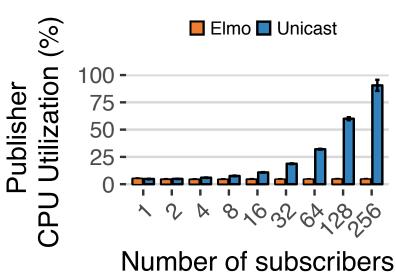


Controller



Applications Run Without Performance Overhead





Conclusion

Elmo

Source Routed
Multicast
for Public Clouds

- <u>Designed</u> for multi-tenant data centers
- Compactly encodes multicast policy inside packets
- Operates at hardware speed using programmable data planes

Learn more here:

Is this a good usecase of programmable dataplanes?

What are the limitations?

Your opinions

- Pros
 - Scalable multicast support hundreds of thousands of tenants
 - Devises different mechanisms for this.
 - Exploits datacenter topology structure
 - Compact headers
 - Low overhead
 - Good application of P4
 - Adhere to constraints of programmable switches.

Your opinions

- Cons
 - Too specific to fat-tree topology
 - What happens under link/port failures?
 - Is the controller churn actually smaller than conventional approach?

Your opinions

- Ideas
 - More general topology
 - Inter-datacenter multicast

Other networking usecases

- Load balancing:
 - HULA: Scalable Load Balancing Using Programmable Data Planes, SOSR'16
- Congestion control:
 - Evaluating the Power of Flexible Packet Processing for Network Resource Allocation, NSDI'17
 - Support RCP and XCP on programmable switches
 - HPCC: High Precision Congestion Control, SIGCOMM'19
 - Obtain precise link information for congestion control
- A new protocols for more efficient L2 switching
 - The Deforestation of L2, SIGCOMM' 16

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