Course Description: Analysis and design of analog circuits in CMOS technology. Emphasis is on fundamental understanding and development of design intuition using both rigorous analysis and back-of-the-envelope approximations. Topics include single- & multi-stage amplifier design, feedback theory, compensation techniques, electronic noise, distortion, switched-capacitor circuits, comparators, output stages, and their application to the design of larger systems such analog to digital converters.

Classroom: ECE 2013  
Class time: T/TR 9:30–10:50am  
Instructor: Pavan Kumar Hanumolu, hanumolu@illinois.edu  
Office: CSL 413, Ph: 217-300-6579  
Office hours: Tuesday 11am–Noon (or by appointment)  
Textbook: No text book required  
Prerequisites: ECE483 or equivalent  
Course website: http://courses.engr.illinois.edu/ece581/  
TA: Tejasvi Anand, tanand3@illinois.edu  
TA Office hours: Wednesday 2:00-3:00pm in CSL 422

Grading:

Homework/Mini projects ................................................. 40%  
Midterm (Nov. 17, 9:30-10:50am, TBA) ................................ 30%  
Project (Due date: Dec. 10, 11:59pm) ......................... 30%

Reference books:


Academic Honesty: Students should follow the UI standards of academic integrity described at http://www.admin.illinois.edu/policy/code/article1_part4_1-401.htm