NORA

Φ Block: Evaluates when Φ is 1
Precharges when Φ is 0

Rules:
- If logic block is pure static CMOS, the # of inversions between latches must be even.
- If dynamic gates are present, the # of static inversions between input latch & dynamic gate must be even.
- The # of static inversions between the last dynamic gate in the block & the output latch must be even.
Combinational logic

(a) CLK-module

Latch

(b) $\overline{CLK}$-module