Instructor: Prof. Arijit Banerjee, arijit@illinois.edu
Lectures: Tuesdays 11:00 am - 11:50 am, 3020 ECE Building
Course TAs: Phuc Huynh, Dipanjan Das, Joseph Jatarski, Leslie Cheng

All lab sessions are in 4024 ECE Building
Lab Session AB2: Wednesdays 3:00 pm-5:50 pm TAs: Joseph Jatarski, Phuc Huynh
Lab Session AB3: Thursdays 12:00pm-2:50pm TAs: Dipanjan Das, Leslie Cheng
Lab Session AB4: Thursdays 3:00pm-5:50pm TAs: Phuc Huynh, Leslie Cheng

Course Website: http://courses.engr.illinois.edu/ece469
Piazza: piazza.com/illinois/fall2018/ece469
Notebook: Must be bound, with pre-printed page numbers. Several types meeting these requirements are available in each of the bookstores.

Purpose: To learn how to design, build, test, and debug power conversion circuits. This course supplements analysis tools presented in ECE 464 with practical design and measurement experience.

Experiments: (See course calendar for lab schedule and report submission deadline)

Experiment #0: Introduction to the Laboratory
Experiment #1: Basic Rectifier Circuits
Experiment #2: Ac-Dc Conversion, Part I: Single-Phase Conversion
Experiment #3: Ac-Dc Conversion, Part II: Polyphase Conversion
Experiment #4: Dc-Dc Conversion, Part I: One-Quadrant Converters
Experiment #5: Dc-Dc Conversion, Part II: Converters for Motor Drives
Experiment #6: Dc-Ac Conversion, Part I: Voltage-Sourced Inverters
Experiment #7: Dc-Ac Conversion, Part II: Pulse-Width Modulation Inverters
Experiment #8: Real Components, Part I: Models for Real Capacitors and Inductors
Experiment #9: Real Components, Part II: Switching Devices and Gate Drive Circuits
Experiment #10: PWM Generation and Control
Experiment #11: PWM Signal Generation with PI Control

Lab Reports: Based on the above experiments you will need to write and submit six experimental reports: one each for Exp #0, Exp #1-#3, Exp #4-#5, Exp #6-#7, Exp #8-#9, and Exp #10-#11.

Grading: Grading is on an absolute scale. Weightings are as follows:

<table>
<thead>
<tr>
<th>Component</th>
<th>Weighting</th>
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<tbody>
<tr>
<td>Pre-labs</td>
<td>10%</td>
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<tr>
<td>Report #0</td>
<td>5%</td>
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<tr>
<td>Report #1</td>
<td>5%</td>
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<td>Report #2</td>
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<td>Report #3</td>
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<td>Report #4</td>
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<td>Report #5</td>
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<tr>
<td>Design Review</td>
<td>5%</td>
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<tr>
<td>Design Project Report</td>
<td>25%</td>
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<tr>
<td>Final presentation</td>
<td>5%</td>
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<tr>
<td>Notebook and in-lab work</td>
<td>7%*</td>
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Total: 100%
*In addition, notebook effort is reflected in report grades.

The plus and minus system will be used, and grades are assigned on an absolute scale as follows:
A   88% and up  (A- 88-90%)  
B   78% to 87% (B + 85-87%,   B- 78-80%)  
C   67% to 77% ( C+ 75-77%,  C- 67-69%)  
D   55% to 66% (D + 64-66%,  D- 55-57%)  

I reserve the right to adjust these numbers downward (in your favor), but they will not increase.

**Additional Course Information**

1. The first pages of the Lab Manual discuss lab reports and other important issues.

2. Assignments:

   a. When you enter each lab session, bring your completed pre-lab assignment to turn in. These are there to help you prepare for the experiment and the report. In some cases, a pre-lab assignment will be handed out in class, in place of the assignment described in the lab manual. They are due as the lab starts. Since they are intended as advance preparation, late pre-labs are not accepted for grade. This includes late arrivals.

   b. Record data, observations, and about anything relevant, in your lab notebook. This should be a log of lab activity, as well as a complete data record. Your notebook must be bound, and must have pre-printed page numbers. Loose data sheets are not acceptable. Plots, copies of commercial data sheets, or other extra information should be taped or pasted into the notebook. The notebook must be kept in ink.

   c. Reports. See the lab report template on the webpage for details. Except for the final design project report, these must not exceed 7 single-spaced, double-column pages total. Reports are to be done individually, using your group data. In each experiment, there are Study Questions to help guide your discussion. Reports are due at the end of your lab period during the week shown on the Schedule. Late reports are penalized 5% per calendar day.

   d. Design Project. During Experiment #9, groups will be assigned specifications relating to the design, assembly, and testing of a complete power supply or power electronic circuit. You must present your paper design in the mandatory design review scheduled during Experiment #11. The final results will be presented briefly in class through an oral presentation with slides. The design report covers more lab effort than others, and should be more complete to reflect this. Reports are due during Finals Week. Please see course calendar.

3. **A key suggestion:** Pace yourself. If you work on your report over a couple of weeks as the experiments proceed, you will find the effort to be quite manageable. This is especially important during the design project.