

# Concealed Bike Anti-Theft Device: Design Document

**\*\* DRAFT \*\***

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## 1 Introduction

### 1.1 Problem

College students frequently face the issue of bikes being stolen on campus. Even with the strongest of protections, they are not completely capable of deterring thieves who have various equipment. Our own member, Elizabeth, had her bike secured with a substantial solid steel U-lock, but was stolen regardless last fall, when someone apparently sawed through the u-lock at night.

In order to discourage bike theft, it would be useful to have a bike anti-theft device that could enable the user to track the bike's location wirelessly.

### 1.2 Solution Overview

We propose a concealed bike anti-theft device that helps bikers prevent their bike from being stolen by unlocking to a specific RFID key (therefore difficult to mimic), providing GPS tracking so the user has the ability to track down the bike if stolen, and frightening away potential thieves with a loud noise when theft is detected.

The device will receive GPS coordinates and periodically transmit the coordinates over LoRa to be received by the user. To allow the bike system to differentiate between potential thieves and the owner, the owner of the bike will have an RFID tag such that while the user is on the bike, the RFID tag can be detected. If the bicycle is moved when the RFID tag is not nearby, it will trigger a loud and annoying alarm to scare away the potential thief.

The device will be designed in a manner such that it is difficult to find and remove, discouraging potential bike thieves from removing the bike. Additionally, the device will be designed to fit a relatively universal bike structure, which will allow the device to be attached to any bike. A standardized device will be more convenient for users as bikes with integrated tracking systems tend to be very expensive.

Our device will be small, rechargeable, battery-powered, and enclosed in a weatherproof enclosure that can easily be attached to most bikes.

### 1.3 High-Level Requirements

1. If a user tries to remove the bike from a stationary location without the RFID tag, the alarm will sound.
2. The device receives GPS data *at least* once per minute, and records its own position over time.
3. The device transmits its GPS location data and additional data over LoRa to be received by a base station.

## 2 Design

### 2.1 Overall System Block Diagram

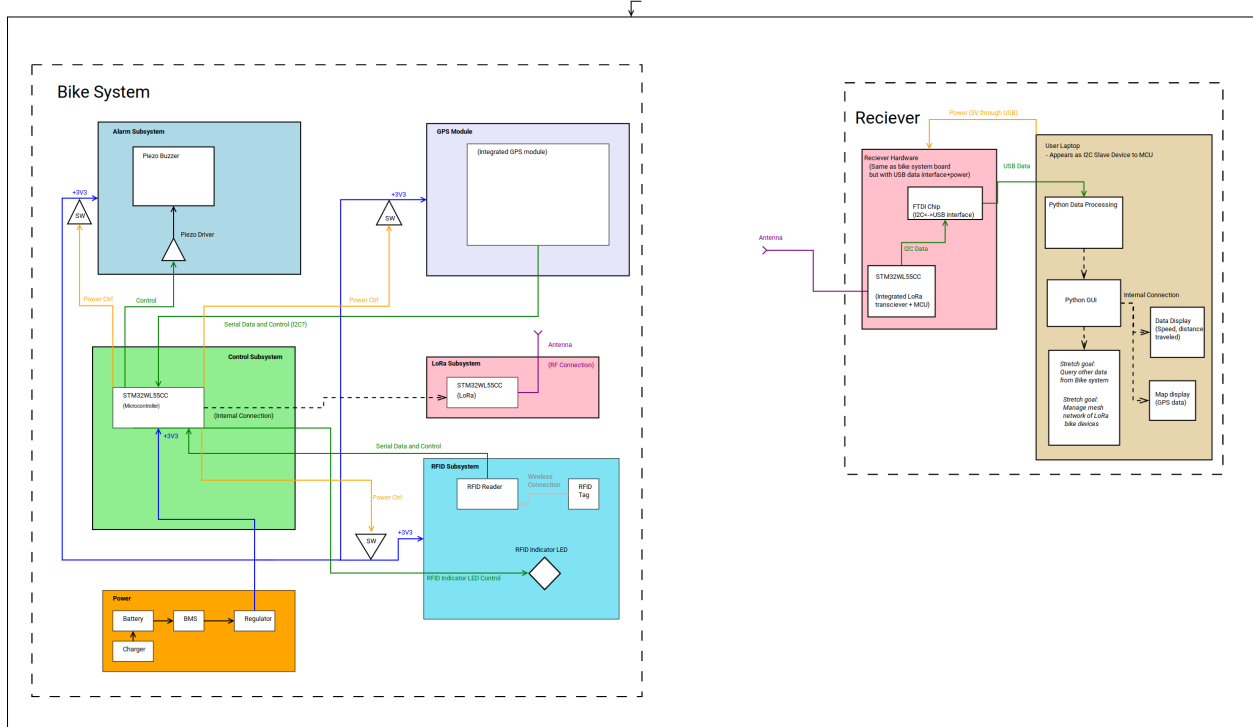


Figure 1: Full Block Diagram

### 2.2 Physical Design

### 2.3 Subsystem Designs

#### 2.3.1 On-Bike Control System

We will use the STM32WL55CC, an SoC that includes both a dual-core ARM microcontroller and a wireless transceiver [7]. The microcontroller will be responsible for buffering data from the GPS receiver, packetizing the data to send over LoRa, and internally transferring the data to the integrated RF transceiver. The microcontroller will also manage the control signals for the alarm subsystem, RFID subsystem, and GPS module.

Since this is a battery-powered device, the microcontroller will also be responsible for implementing power-saving measures. For example, the microcontroller will vary the number of GPS data transmissions depending on the bike state: while the bike is in a “idle/safe” state (stationary in one location) it will only transmit its location over LoRa once per hour. If the device is in the “stolen” state, it will transmit its location over LoRa once per minute. The microcontroller will also cut off power to the Alarm when it is not in use, and as a stretch goal, the microcontroller will selectively cut power to the RFID/GPS subsystems in an intelligent manner (ie, the RFID transmitter will not continuously transmit/listen for the RFID tag if the GPS module detects that the bike is in motion, and the motion was initiated by the correct user).

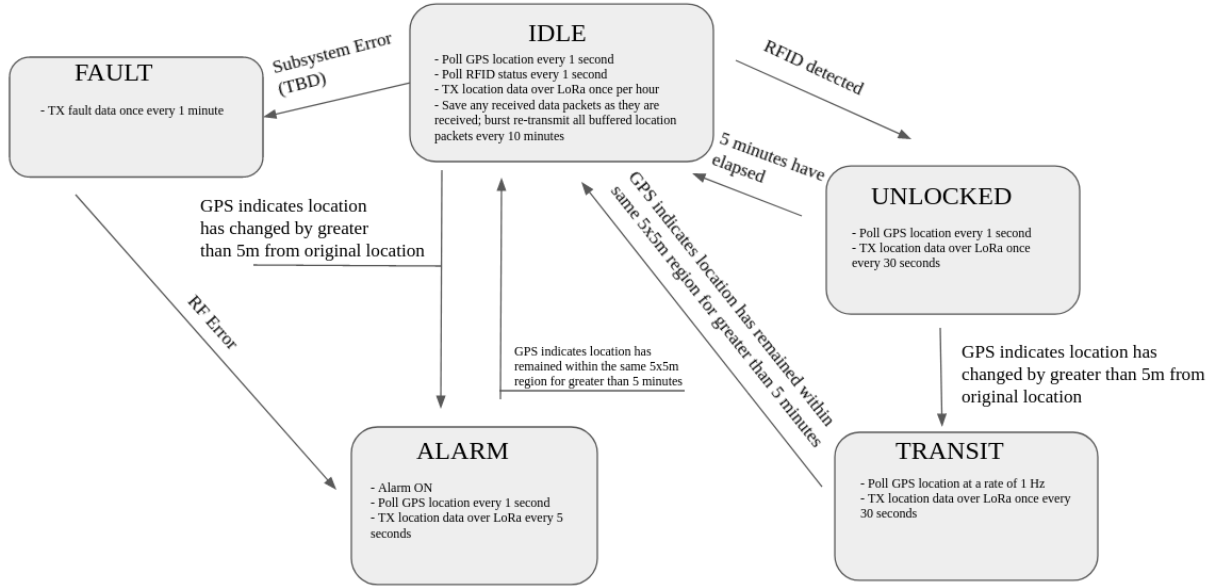


Figure 2: Control System State Machine

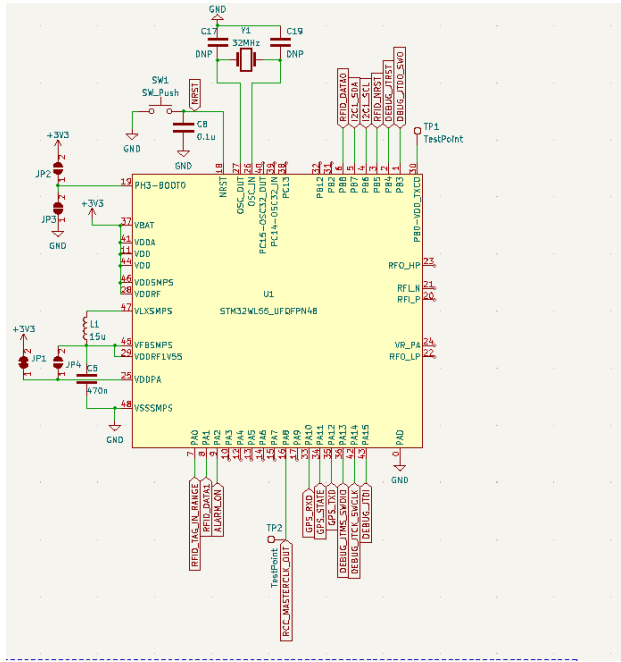


Figure 3: Microcontroller and Support Circuitry Schematic

### Control Subsystem Requirements:

1. The microcontroller implements a state-based control system with power-saving measures in the idle/safe states.
2. The microcontroller packetizes data to be sent over LoRa at an update rate of *at least* once per hour in “idle” states, and *at least* once every two minutes in “active” states.

3. *Stretch goal:* The microcontroller manages multi-unit mesh networking by re-transmitting received data within 1 second of receiving the data. (See the **LoRa Subsystem** section for more detail on mesh networking).

### 2.3.2 LoRa Subsystem

We will use LoRa, a low-power spread-spectrum RF protocol, to transmit GPS data from the bike device to be received by the user. LoRa is an ideal choice for this application because it is extremely low-power, has a range of several miles even in crowded city conditions, and does not require any radio licensing to operate [2]. These qualities will enable our device to function for extended periods of time on battery power, allow the signal to be received by the base station for multiple miles, and allow any user to purchase this device and use it without worrying about licensing.

The LoRa transceiver is integrated with the STM32WL55CC SoC, so the LoRa subsystem will require us to design an external matching network and antenna [6]. We will choose a compact antenna and conceal it to prevent theft (for example, inside a hollow conventional plastic reflector that can be mounted on the handlebars or seat; refer to figure 4 for an example of one type of bike reflector). We will design the matching network to ensure reflected power is within the STM32WL55CC's maximum reflected power parameter.

As a stretch goal, we will implement a rudimentary form of mesh networking between bike tracking devices. Each bike tracker will listen and re-transmit GPS coordinates it receives from other bike trackers – as a result, the range of the LoRa bike tracker will be dramatically increased, since its GPS coordinates can be received as long as *any* bike tracker is in range instead of only if the base station is in range.

#### *LoRa Subsystem Requirements:*

1. The LoRa subsystem matching network and antenna have a VSWR of less than 10 at the RF output of the STM32WL55CC.
2. The antenna is hidden in a common bike component such as a reflector so that it is not visibly obvious.

\*\*\*\*\* RF MATCHING AND FILTERING NETWORK SCHEMATIC HERE \*\*\*\*\*

### 2.3.3 GPS Module

We will use an off-the-shelf GPS module to receive GPS data. We will choose a GPS module that can receive GPS updates at a rate of at least 1 Hz. We will mount the GPS module on the bike such that the GPS antenna is able to see a clear view of the sky, but is concealed from the casual observer. We will likely conceal the small patch antenna inside of a plastic bike reflector such as the type shown below.<sup>1</sup>



Figure 4: Example of a plastic bike reflector.

#### *GPS Subsystem Requirements:*

1. The GPS module can communicate with the microcontroller at a update rate of greater than 1 Hz.

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<sup>1</sup>We are not sure if bike reflectors have metal foil inside the plastic – if this is the case we will obviously remove the metal foil before mounting the antenna.

### 2.3.4 RFID Subsystem

We will use an off-the-shelf RFID tag and an RFID reader module. The RFID module will listen to determine if the bike owner's RFID tag is present, and transfer the RFID data to the microcontroller. The microcontroller must be able to read the RFID identifier from the transferred data to determine if the owner's RFID tag is present (so that the device does not just trigger when any RFID is present).

To increase ease of use, we will include a visual indicator such as a concealed LED to show whether or not the correct RFID tag has been detected so that the user doesn't accidentally set off the alarm. The visual indicator will be concealed in a common bike component or unobtrusive location.

#### ***RFID subsystem requirements:***

1. The RFID detector will detect the user's RFID tag within 10 seconds.
2. The RFID subsystem will provide a visual indication that the RFID tag has been detected within 1 second of detecting the RFID tag.
3. The RFID subsystem does not disable the alarm unless the user's RFID is detected.
4. The RFID indicator LED is concealed in an unobtrusive location on the bike.

### 2.3.5 Power Subsystem

We will power the device with three AA batteries in series to provide approximately 2700 mAh at 4.5V [8]. We will implement a basic battery management system to ensure the batteries do not reach an unsafe state. As a stretch goal, we will include USB-C recharging capabilities. We will use linear regulators to provide a smooth 3V3 supply voltage for all subsystems.

\*\*\*\* POWER CIRCUIT SCHEMATIC HERE \*\*\*\*

#### ***Power subsystem requirements:***

1. The power system will provide  $3V3 \pm 0.4$  mV to all subsystems.
2. The power system will power the device for over 24 hours.

### 2.3.6 Alarm Subsystem

We will use a simple Piezo buzzer as the alarm. The alarm will be driven by the driver circuit shown in figure 5. As shown in the diagram, when the FET is turned off, the circuit draws negligible current, thus minimizing power draw.

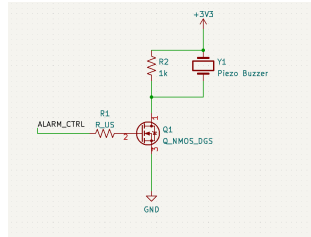


Figure 5: Basic Piezo buzzer driver circuit.

### ***Alarm Subsystem Requirements:***

1. The alarm makes noise when triggered by the ALARM\_CTRL signal.
2. The alarm circuit draws  $< 1 \mu\text{A}$  of current when not making noise.

### **2.3.7 Base Station Subsystem**

Since we have limited resources for PCB development, our main PCB is designed such that it can be used for both the on-bike system and the base station receiver system with minor (and non-destructive) modifications (ie, jumpers and DNP components). Like the on-bike control board, the base station system will use the STM32WL55CC integrated transceiver and microcontroller to control the device and receive LoRa. We will use an FTDI I2C  $\leftrightarrow$  USB IC to allow the microcontroller to communicate with the user's laptop – the laptop will appear as an I2C slave device to the microcontroller, and the microcontroller will continuously transfer data to the computer as the data is received. To increase ease of use, the base station will also be able to be powered over USB.

On the computer, the user will be able to access the data via a GUI likely implemented in Python. The GUI will allow the user to access historical GPS data, and see recent GPS data plotted on a map.

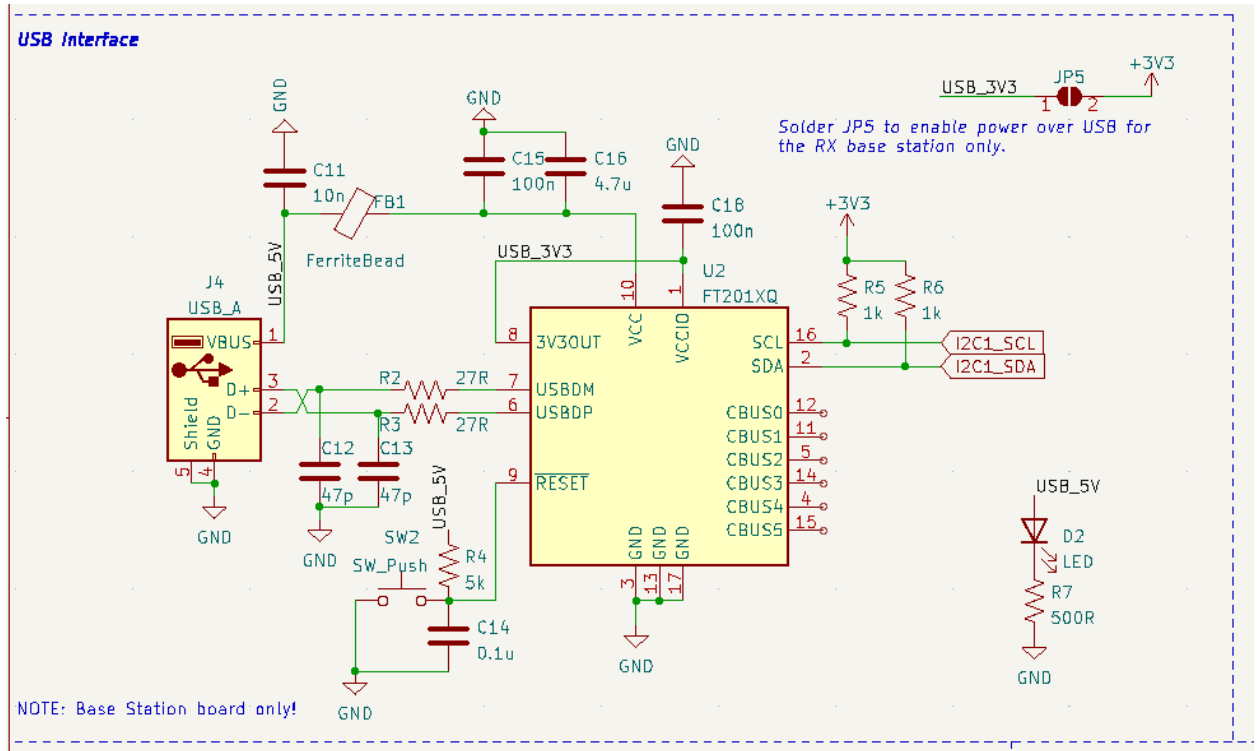


Figure 6: USB Interface Schematic.

### ***Base Station Subsystem Requirements:***

1. The receiver board will receive LoRa packets from the bike-based device.
2. The receiver board will interface with the computer over USB.
3. The GUI will display GPS data over the past day, week, month, or year on a map.
4. The GUI will allow the user to view plots of the average speed and distance traveled.

## 2.4 Tolerance Analysis

Since this is a battery-powered device that is meant to be unattended for relatively long periods of time, it is critical that our power system can supply adequate power for the device over a period of time. Our battery pack will provide approximately 2700 mAh, so we must calculate the total current draw for our device to ensure we can source enough power to power the device for an appropriate period of time.

Subsystem	Current Drawn	Duty Cycle Estimate
Microcontroller/LoRa, high power TX [7]	120 mA	5 %
Microcontroller/LoRa [7], Idle	3.45 mA	100 %
GPS module [3]	50 mA	100 %
RFID module [4]	45 mA	100 %
Alarm ON	3.3 mA	1 %

Figure 7: Power Budget Analysis

From the chart above, we can see that the full current drawn during operation (assuming 100% duty cycle for the RFID and GPS module) is:

$$120(0.05) \text{ mA} + 3.45 \text{ mA} + 50 \text{ mA} + 45 \text{ mA} + 3.3 \text{ mA} = 107.75 \text{ mA}$$

Since we will be using linear regulators, we can assume that the current drawn by the load is the same as the current drawn from the battery pack, and do the power calculation in terms of available milliamp-hours.

Since the 3 AA batteries can provide 2700 mAh:

$$\frac{2700 \text{ mA} * \text{hours}}{107.75 \text{ mA}} = 25 \text{ hours of operation}$$

This is acceptable, but ideally, the device would be able to autonomously operate for multiple days without being recharged. We can increase the operation time significantly by limiting the on-time of the RFID and GPS modules. With a duty cycle of 25 % for both, we can increase the operational time, as shown below:

Subsystem	Current Drawn	Duty Cycle Estimate
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RFID module [4]	45 mA	25 %
Alarm ON	3.3 mA	1 %

Figure 8: Power Budget Analysis: Decreased Duty Cycle

$$120(0.05) \text{ mA} + 3.45 \text{ mA} + 50(0.25) \text{ mA} + 45(0.25) \text{ mA} + 3.3 \text{ mA} = 36.5 \text{ mA}$$

$$\frac{2700 \text{ mA} * \text{hours}}{36.5 \text{ mA}} = 74 \text{ hours of operation}$$

74 hours of operation is still somewhat low, but more reasonable than a single day. As we proceed with the design, we will work to increase the operational time with a single charge even further by implementing power-saving measures on the device, adding another 3-cell battery pack in parallel, or using a higher-capacity battery.

### 3 Ethics and Safety

This project is designed with IEEE's code of ethics in mind, and the main point as we work on the project is "to hold paramount the safety, health, and welfare of the public, to strive to comply with ethical design and sustainable development practices, to protect the privacy of others, and to disclose promptly factors that might endanger the public or the environment" [1].

A proponent safety concern among electrical design is the battery. With the consideration of including a rechargeable battery, proper use and design may lead to battery failure - potentially leading up to explosion. As such, we work to limit the risk by ensuring that the amount of power drawn and the cell voltage are maintained within the safety limit of the battery used through the use of a power regulator. We are also making sure that the design accounts for the battery being maintained and operated in safe operating conditions. Battery Charge/Discharge?

Furthermore, the design is implemented with LoRa and RF identification, which involves the transmitting and receiving of information wirelessly. As such, an ethical consideration that is paramount to our objective is that data is received accurately. This is important because if given false or failed readings thieves can make off with the bike even with the flawed design attached due to failure or misreading of information. As such, we plan to ensure that the correct RFID tag can communicate with the appropriate transmitter such that the alarm will sound if and only if the bike is in motion and reaches out of range of the appropriate RFID.

### References

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- [6] STMicroelectronics, "RF matching network design guide for STM32WL Series", AN5457, 2020.
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