Lab Protocol

Please read through the entire lab prelab and procedure BEFORE attending your lab class. Things go much smoother if you know what to expect and have questions ready for your TA. To make sure that you start thinking about some of the issues you will encounter, each lab (except the first one) has an associated prelab assignment.

- **Prelab** - Before the upcoming lab each week, you must answer all the prelab questions. At your prescribed lab time, you will enter 1001 ECEB (the lab) through 1005 ECEB (lecture room next to the lab). You will have five minutes to place these items on the center bench:
  - Your prelab document
  - Your lab summary page (for prelab grading)
  - Any hardware constructed for that prelab exercise

You need to return to 1005 ECEB for instruction from one TA while the others grade your prelab. If you come late to lab (after prelab grading has begun) you can expect to lose points.

- **Lab** – After instruction, you will return to the center benches in the lab, 1001 ECEB, and begin the student Breakout Session before completing the remainder of the lab procedures. You will be following the lab procedures in this manual. Read the directions and answer the questions using full, understandable sentences giving the answer AND an explanation of why you gave your answer, all in the space provided. You will turn in a lab summary but retain your lab procedures at the end of each lab session.
Grading

- **Lab Experiments** - There are 9 guided labs that teach you how to use the lab equipment, conduct engineering analysis of circuits, and teach you the skills necessary for the end of semester design challenge. Each week, you earn 8 points that are divided between the prelab (2 points), breakout participation for the day (2 points), active participation with your lab partner (2) and the lab Summary (2 points). The score, therefore, includes a measure of your apparent willingness to work with your lab partner, talking through the questions, and assisting in building the circuits and taking the measurements. The Unit 1 report is worth 16 points (4 per lab for labs 1-4) and the Unit 2 report is worth 20 points (4 per lab for labs 5-9).

- **Final Design** - During the final weeks of the class you will complete an engineering design challenge and submit documentation in the form of a project proposal and a final report plus a lab summary for Exercises 10, 11, and 12. The final design challenge is not intended to be a “do or die” challenge. Students who produce excellent documentation and demonstrate an understanding of the design process can still receive high marks even if the design fails to work as desired. Failure to attend any session during the Final Design period will result in an automatic LOSS of 12 points from your semester score in addition to the loss of points of anything due that day. Failure to follow procedures or do the requested exercises will also result in a loss of points from your semester score.

- **Modules** - There are modules provided as you go procedure through the semester that will augment your learning. These modules will concentrate on items like fundamental science, electronic devices, benchtop tools, circuits or microprocessor usage and are available on the course website. A minimum of 10 modules will be turned in through the semester for 5 points each and are graded on a complete/incomplete scale. While you may work ahead on Modules, due dates for minimum number of completed modules will be set by your TA throughout the semester.

- **Participation** - Most importantly, participation points, to be assigned by your TA, are used to assess the level of involvement of each student during the labs and design challenge. Participation includes (but is not limited to) coming to lab on time, cleaning up your station after each lab, not hoarding lab equipment/parts, and simply putting in effort to learn something from each lab.
# Laboratory Grade Breakdown

<table>
<thead>
<tr>
<th>Assignment Type</th>
<th>Assignment</th>
<th>Due Date</th>
<th>Value</th>
<th>Maximum Points</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lab Exercises</td>
<td>Exp. 1-9 Prelab/Breakout/Summary/Activity</td>
<td>Each week</td>
<td>8 points each = 72</td>
<td>108</td>
</tr>
<tr>
<td>Unit Report #1</td>
<td>Exp. 1-4</td>
<td>At beginning of Lab 6</td>
<td>16</td>
<td></td>
</tr>
<tr>
<td>Unit Report #2</td>
<td>Exp. 5-9</td>
<td>At beginning of Lab 11</td>
<td>20</td>
<td></td>
</tr>
<tr>
<td>Final Design</td>
<td>Initial Proposal (Prelab 10)</td>
<td>At beginning of lab 10</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Exp. 10, 11, 12 Summary</td>
<td>End of each lab</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td></td>
<td>(Final) Proposal</td>
<td>At beginning of Lab 11</td>
<td>12</td>
<td>80</td>
</tr>
<tr>
<td></td>
<td>Final Report</td>
<td>Midnight 2\textsuperscript{nd} day after demo</td>
<td>40</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Demonstration</td>
<td>Last full week of classes</td>
<td>20</td>
<td></td>
</tr>
<tr>
<td>Modules</td>
<td>10 Modules</td>
<td>As instructed by TAs, none accepted after demo day</td>
<td>5 points each</td>
<td>50</td>
</tr>
<tr>
<td>Participation</td>
<td>Daily attendance, participation, effort</td>
<td>Assessed throughout, determined at end.</td>
<td>20</td>
<td>20</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td><strong>Total</strong></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td><strong>258</strong></td>
</tr>
</tbody>
</table>
Academic Integrity

You learn well by collaboration (working together). The way this course is designed, you do not learn well by sub-dividing the work and each student working separately. Collaboration between students on all lab assignments (including prelabs) is encouraged and in many cases, required. While this means that the answers you provide in your lab experiments should be similar to your partner, they should not be exact copies. All answers should be put into your own words and work must be shown for any problems involving calculations.

Plagiarism carries serious penalties, ranging from a grade of zero on the assignment to failure for the course. In all cases of plagiarism, a letter will be filed with the Dean of the student’s college. If you are unsure if your work too closely resembles that of another student, ask your TA about it before turning it in. For further information about academic integrity please see Article 1, Part 4 of the University of Illinois student code (http://studentcode.illinois.edu/). Any students who may be retaking the course must complete a final project significantly different than in prior semester(s).

Lab Partnering

In addition to the strictly academic goals of this lab, you will also learn other valuable, less tangible skills, one of which is how to work constructively with other people. Most of you will be working closely with one other person. Students pair up at the beginning of the semester and maintain this relationship until the end. However, there are also partnerships that are dysfunctional and cause problems which get worse towards the end. So a word of caution - if you are initially paired with someone who is not a good match for you, find another bench-mate as early in the semester as possible. You can talk to Prof. Schmitz or your TA about partnering issues without negative consequences. We will try to accommodate your needs even if this means allowing you to change sections.

Locker Policy

Each group in ECE 110 lab will be assigned a locker for storing your car chassis and shared breadboard. Students are NOT ALLOWED to keep BNC/banana plug cables, alligator clips, test boxes, batteries and any other item from the lab that you have not been given express permission to store in your locker. Lockers will be checked regularly by the TAs and any violation of the above rule will be recorded and penalized by taking off participation points. You are actually encouraged NOT to use the lockers to store your electronics kits. Prelabs will generally require that you have your kit with you and that you build a circuit before coming into lab.
Absence Policy

Labs meet every week except for the partial week of MLK Day and Labor Day holidays and Spring and Fall break. Absences effect more than just you and your grade. You will more-than-likely have a lab partner who will be placed at a disadvantage each time you are not present. Even worse, your lab partner may come to lose trust in you and your willingness to be a productive member of the team. You may not choose to miss a lab in order to complete an assignment for another course, study for the exam of this or another course, or to take an exam for another course. When an exam conflicts with your regularly-scheduled lab, that course must be willing to offer a conflict exam.

A single lab absence may be excused if you

1. Alert your TA to your absence as soon as you are able. This should be done at least one week in advance for planned trips, just before the lab for illnesses, or as soon as you are stable for injuries.
2. You obtain permission from your TA specifically for the Saturday open lab or an open bench in another lab session for purposes of making up the missed laboratory work. You must make it up before your next lab session.

For planned trips, you should do the lab procedure during the Saturday open lab period or in another lab section with an open bench prior to the week in which your section will be doing that same procedure. If the TA decides the missed lab is not an excusable absence, you will still need to complete the lab for grading, but will lose a minimum of 10 points from your semester total. A second unexcused absence will cost an additional 25 points from your semester total and additional unexcused absence will each cost an additional 50 points from your semester total. Failure to make up a missed lab prior to the next lab meeting will cost 10 points per week until you have fully caught up. The entire semester lab earns only about 250 points, so missing lab is clearly a disadvantage.

The severity of this penalty is largely because of the ill effect it will have on a lab partner and the hassle it causes your TAs. It is also assessed to students who have no lab partner out of fairness to all.

For lab meetings missed during the final project design, you should work with your lab partner to help catch up on lost time. Points are lost as described above.

Valid long-term illnesses may be treated differently upon review of a letter from the Emergency Dean (Dean of Students) or the DRES center. These will be treated on a one-on-one basis after reviewing the facts of the situation.
Food and Drink
Food and drink are prohibited in the lab, with the exception of water in bottles or containers with tight-fitting screw-top lids. Beverages such as pop and sugary drinks can result in a sticky mess that ruins the furniture while posing an added danger to the (rather expensive) lab equipment, and should never be opened in the lab. Food should NEVER enter the lab.

Any exceptions to the food and drink policy (for instance, due to a diabetic condition) must be explicitly granted by the course director. Violations to this policy may result in deductions from your participation points or in more severe cases, you may be asked to leave for the day and receive a zero on that week’s experiment.

Accommodation Policy:
If you feel that you may need an accommodation based on the impact of a disability, feel free to contact your TA privately to discuss your specific needs. You are all guaranteed an equal opportunity in the class and no student is denied educational access, regardless of disability. If you need assistance or ever feel uncomfortable please see your TA right away. For more information please visit: http://www.disability.illinois.edu/

Disclaimer
All policies and assignments outlined in this syllabus are checked each semester. Anything that conflicts with policy stated elsewhere will be resolved as found during the course of the semester.
# ECE110: Lab Session and Semester Overview

There are 13 weekly laboratory meetings during this semester. The first lab meets for Experiment 1 the first full week of classes. The final lab meets to demo projects during the final “full” week.

A pre-lab exercise must be completed before coming to lab (at least for Experiments 2 through 11!). At each laboratory meeting, you will place your Prelab procedures, Solution and your experiment Summary sheet in the lab (1001 ECEB) and go to 1005 ECEB where class discussion will begin strictly at 5 minutes “after the hour”, that is, discussion begins at 9:05 am, 12:05 pm, 3:05 pm, or 6:05 pm. Students who are not ready for the start of class will likely lose credit for their prelab. The structure of each lab meeting will look roughly like the following table in hour:minute format.

<table>
<thead>
<tr>
<th>Start</th>
<th>End</th>
<th>Time to Complete</th>
<th>Task</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>0:00</td>
<td>0:05</td>
<td>5 minutes</td>
<td>Prelab Setup in 1001 ECEB</td>
<td>Must be back in 1005 ECEB by 5 minutes after or lose points for prelab</td>
</tr>
<tr>
<td>0:05</td>
<td>0:15</td>
<td>15 minutes</td>
<td>Lab Guidelines for today</td>
<td>Time may vary to cover misconceptions</td>
</tr>
<tr>
<td>0:20</td>
<td>0:45</td>
<td>25 minutes</td>
<td>Group Breakout Session</td>
<td>Consult in groups of 8 improving prelab design</td>
</tr>
<tr>
<td>0:45</td>
<td>2:00</td>
<td>75 or more minutes (tentative)</td>
<td>Lab core, Autonomous Car</td>
<td>Work alone or with partner as described to advance your autonomous car design</td>
</tr>
<tr>
<td>2:00</td>
<td>2:30</td>
<td>30 or less minutes (tentative)</td>
<td>Explore More! Module(s)</td>
<td>As time allows, self-select from a group of fun and informative laboratory exercises!</td>
</tr>
<tr>
<td>2:30</td>
<td>2:40</td>
<td>10 minutes</td>
<td>Group Discussion</td>
<td>Consult in groups of 8 to answer summary questions</td>
</tr>
<tr>
<td>2:40</td>
<td>2:50</td>
<td>10 minutes</td>
<td>Cleanup benches and submit your lab summary</td>
<td>The next lab may be arriving to bring in their prelab so it is critical that you not “run long”.</td>
</tr>
</tbody>
</table>

**Table 1**: Procedure for the 2 hours and 50 minutes in a typical ECE 110 lab.

The first eight or nine laboratory exercises are highly-structured, cookbook-style labs where you are strongly guided by the procedure and explicit questions. These core labs are augmented with Learn More! Modules which extend your understanding of the material and/or provide timely resources for moving forward to future labs. Some of these modules may be explicitly required (you must do them during the course of the semester), but most are only semi-required (you choose those which interest you). You will be required to complete at least 10 modules for full credit in the lab.
While the focus of each individual lab appears to explore individual concepts in hardware, circuit theory, and the construction of an autonomous vehicle, the complete set of lab procedures are designed to help you build a skill set in engineering exploration that will serve you well both in this class and throughout your engineering career.

The last few laboratory meetings are design-based where each lab group will design, build, and demonstrate an electronic device to accomplish a task. In this “project-phase” of the course, you are required to utilize the skills developed in your earlier lab meetings to produce a written report.

A short, tentative breakdown of your semester laboratory exercises are provided in the table below.

<table>
<thead>
<tr>
<th>Labs:</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. A Lab Course Introduction</td>
</tr>
<tr>
<td>• Assemble the robotic car chassis</td>
</tr>
<tr>
<td>• Explore the breadboard</td>
</tr>
<tr>
<td>• Play with DC (direct-current) equipment</td>
</tr>
<tr>
<td>• Compare the battery to the benchtop power supply</td>
</tr>
<tr>
<td>• Generate a “zeroth-order-model” of the car’s motor</td>
</tr>
<tr>
<td>• Student Exploration: <em>Explore More! Modules</em></td>
</tr>
<tr>
<td>• DUE: turn in Prelab, Lab Summary and any completed Modules</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>2. Circuit Networks</th>
</tr>
</thead>
<tbody>
<tr>
<td>• Learn to “switch” your motors for control</td>
</tr>
<tr>
<td>• Use current-limiting resistors to slow motors</td>
</tr>
<tr>
<td>• Construct a resistor network for improved power dissipation</td>
</tr>
<tr>
<td>• Student Exploration: <em>Explore More! Modules</em></td>
</tr>
<tr>
<td>• DUE: turn in Prelab, Lab Summary and any completed Modules</td>
</tr>
<tr>
<td>• Returned to you: Lab 1 Summary</td>
</tr>
</tbody>
</table>
3. Circuit Laws and Intro to Time-Varying Signals
   • Learn to “switch” networks for fine-tuned speed control
   • Use the oscilloscope to watch signals change across time
   • Apply Kirchhoff’s Voltage and Current Laws (KVL and KCL) to real circuits
   • Student Exploration: Explore More! Modules
   • DUE: turn in Prelab, Lab Summary and any completed Modules
   • Returned to you: Lab 2 Summary

4. Time-Varying Signals
   • Build a “cloud detector” using engineering design
   • Improve oscilloscope usage
   • Build a motor-drive circuit for improved engineering design through transistors
   • Use a function generator
   • Student Exploration: Explore More! Modules
   • DUE: turn in Prelab, Lab Summary and any completed Modules
   • Returned to you: Lab 3 Summary

5. Oscillator
   • Build an oscillator
   • Use the pulsed oscillator signal for higher-efficiency motor-speed control
   • Student Exploration: Explore More! Modules
   • DUE: turn in Prelab, Lab Summary, and any completed Modules
   • Returned to you: Lab 4 Summary

6. Improved Motor Modeling
   • Explore the definitions of power and energy
   • Build an improved model of the car motor
   • Student Exploration: Explore More! Modules
   • DUE: turn in Unit 1 report, Prelab, Lab Summary and any completed Modules
   • Returned to you: Lab 5 Summary
7. Pulse-Width Modulation (PWM)
   • Alter the oscillator design to produce non-symmetric duty cycle
   • Fine-tune motor speed control using PWM signals
   • Student Exploration: Explore More! Modules
   • DUE: turn in Prelab, Lab Summary and any completed Modules
   • Returned to you: Lab 6 Summary, Unit 1 report

8. Straight-Run Cars
   • Build a circuit for speed control
   • Build a circuit for wheel-speed balance
   • Couple these circuit outputs to the motor-drive for straight-run races
   • Student Exploration: Explore More! Modules
   • DUE: turn in Prelab, Lab Summary and any completed Modules
   • Returned to you: Lab 7 Summary

9. Autonomous and Elegant
   • Plan and build a beautiful circuit
     ✓ Organized layout
     ✓ Intelligent wire-color choices
     ✓ Robust layout not prone to accidents
   • Complete and demo a fully-autonomous maze-running vehicle
   • Student Exploration: Explore More! Modules
   • DUE: turn in Prelab, Lab Summary, and any completed Modules
   • Returned to you: Lab 8 Summary

10. Modules and Project Design
    • Preliminary Proposal due
    • Student Exploration: Explore More! Modules
    • DUE: turn in Prelab Proposal, Lab Summary, and any completed Modules
    • Returned to you: Lab 9 Summary
11. Modules and Project Design
   • Project Proposal due
   • Student Exploration: *Explore More! Modules*
   • DUE: *turn in Unit 2 report, Experiment Procedures 1-9, Project Proposal* and any completed Modules
   • Returned to you: Lab 10 Summary

12. Modules and Project Design
   • Student Exploration: *Explore More! Modules*
   • DUE: *turn in* any completed Modules
   • Returned to you: Project Proposal

13. Project Presentations
   • Presentation by schedule
   • peer review of others
   • DUE: *Project Demo!!*, turn in any completed Modules, Final Report due at midnight 2\textsuperscript{nd} day after demo
   • Returned to you: Unit 2 report, and Lab Procedures 1-9.

*A little about the Final Project*

The key to doing well on the final project is to learn the mechanics of experimentation in electronics. The early labs will prepare you. You will need to be able to

- Characterize a sensor you have never seen before by collecting circuit data.
- Use that sensor to accomplish a task

That should be easy enough. However, your project proposal must be approved by your TA. Your TA will be looking for more explicit details to verify that your project is worthy of pursuit in ECE110 Intro to Electronics. Primarily, it must be of appropriate challenge and content. Structurally, it should very clearly consist of a minimum of these three parts:
Figure 1: Three essential features of any ECE 110 Project.

1. A new sensor (not used in the ECE 110 core lab procedure) and a plan to characterize it such that any engineer has a better idea of how it can and cannot be used in a circuit.

2. An electronic circuit that generates a solution to the task at hand. ECE 110 modules will provide a vast array of circuits for solving a multitude of problems. Any of these may be applied or you can even find others in text books and online. Imitating a solution done elsewhere is allowed, but you must not merely copy their solution, you must use their proposal as only a guideline while using the parts available to you to generate your own design. Have a plan for analyzing this circuit...often by controlling the input using a function generator and observing the output on the oscilloscope.

3. An output that clearly shows the functionality of your design. Most commonly, these may be an array of LEDs, car motors, servo motors, or a loudspeaker, but an output that appeals to any of the human senses is acceptable!

4. Students commonly form a strong misconception about this course and the requirements of the final design project. ECE110 is not a programming course. Rather, it provides limited exposure to some scientific computing basics (using software to plot and model) and some physical computing basics (using Arduino as a measurement device and a digital controller). Only plotting (graphing) is a requirement in the final project. Other use of physical computing in the final project is purely up to the student team as most projects do not necessitate the use of a digital controller. In other words, feel free to use Arduino as part of your solution, but removal of the Arduino should still leave the three pieces (demonstrated in Figure 1) intact.

What about grading?

- Analyze your data using a graph and use that analysis to model its behavior.
- Show proficiency with the equipment used to collect/analyze data.
- Properly document your procedure, data, observations, summary and conclusions in a well-written report.

The TAs will use a grading rubric on your final project demonstration and another on your final project report.
The final demonstration rubric will look something like this:

<table>
<thead>
<tr>
<th>Student #1</th>
<th>Points Possible</th>
<th>Score Given</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ability to explain in technical terms (language, course objectives)</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>Use of the oscilloscope, other equipment (tech)</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>Individual features (partial-functionality/troubleshooting)</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>Full-system demo (functionality)</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>Overall presentation appeal (planning, execution, equity)</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>Extra Credit (&quot;elegant solution&quot;)</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>Total (/20):</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The final report rubric will look something like this:

Total = 40 pts

1. Introduction [5 pts]
   a. Problem description [2.5]
   b. Design concept [2.5]
2. Analysis of components [15 pts]
   a. Sensor characterization [7.5]
   b. Design considerations [7.5]
3. Design description [15 pts]
   a. Block diagram and explanation [5]
   b. Circuit schematic and explanation [5]
   c. Design considerations [5]
4. Conclusions [5 pts]
   a. Lessons learned [2.5]
   b. Self-assessment [2.5]

If your final project deviates greatly from your proposal, points will be lost if the changes make the project deviate from the ECE 110 objectives.
Course Notes: Etiquette and Safety

Etiquette
Etiquette is the set of rules or procedures that define polite behavior within a particular setting. The etiquette for the ECE 110 Lab is outlined below and has been specifically designed to foster a healthy and productive environment in the laboratory. Please take the time to read and follow the lab etiquette so that you and your peers will have the best possible experience in the lab.

Respect Your Peers
In the field of engineering, most projects are too large for any one engineer to accomplish on their own. As a result, most engineering projects (both the academic and professional) require the collaborative efforts of a team of engineers. Effective collaboration is not possible without respect between peers. Proper respect means that you should not show up late, sneak out early, or repeatedly miss lab periods.

Since the University of Illinois is both large and prestigious, you will find that your classmates come from wide variety of backgrounds. Although differences in cultural background or personality can sometimes lead to tension between peers, a diverse team benefits greatly from the diverse ideas it generates. Your classmates will approach problems differently and have strengths that are different from your own. Embrace your diversity, learn from the strengths of others, and allow others to learn from you.

Cooperate with Your Lab Partner
Laboratory courses are intended to be a place where students can gain “hands-on” experience with engineering concepts. Take turns with your lab partner leading hands-on activities within the lab. This should be done when wiring the breadboard, using laboratory equipment, and in using the computer for data analysis and graphing.

If you find that your partner is not participating enough or perhaps not letting you participate enough, communicate the issue to your partner or discuss the issue with one of your lab instructors.
**No Food or Drink**

Food and drink are generally prohibited in the lab, with the exception of bottles of water with a tight-fitting screw-top lids. Other beverages such as pop and sugary drinks can result in a sticky mess that ruins the furniture while posing an added danger to expensive lab equipment. Food should **NEVER** enter the lab. Besides the risk of spreading germs through the practice of eating in a hands-on laboratory, there are other issues with respect to toxic-poisoning discussed below in Safety Considerations. Furthermore, it makes the lab look sloppy and unprofessional to those who observe through the windows and the financial supporters of ECE and its laboratory facilities.

Any exceptions to the food and drink policy must be explicitly granted by your head lab instructors. Most TAs will allow you to have food and drink in 1005 ECEB (the lecture area adjacent to the lab). Please finish your snack and dispose of trash **before** going into the lab as 1005 ECEB also gets used for office hours during lab time. Thanks!

**Respect Property and Lab Equipment**

Through each lab session, do your best to be respectful of the workspaces being used by your classmates and minimize clutter that might serve as a tripping hazard. At the end of each lab, your workbench (and any common bench areas you have used) should appear as if you had never been there. Place cables in their proper storage location, put away your circuit components and toss any waste into the trash can. A neat and clean workbench is much easier to use when troubleshooting. Show respect to the students in the other lab sections by helping them keep the lab clean!

**Collaborate with Classmates**

Technical discussions and collaborative learning are among the most effective ways to build and affirm your command of the material. Discuss your experiment procedures, questions, and observations with other lab groups. Take time to talk with your neighbors regularly to ensure good scores on your assignments.

**Cheating**

Collaboration does **not** imply that you may submit work done by your lab partner or another classmate. Collaboration is a give-and-take procedure where two-way discussions enhance student understanding of the material. One-way conversations (like asking for an answer to a question or copying from your teammate or another student’s lab report) is just **cheating**. Any student caught cheating will be subject to disciplinary action.

The following list contains just a few actions prohibited in ECE110 and subject to disciplinary action: copying pre-lab answers from your lab partner or another student in ECE110, submitting data recorded by another lab team, submitting code or graphs...
written or produced by another lab team from this or any past semester. Furthermore, you must not mislead the grader with respect to the amount of design work accomplished by your team. For example, while it is generally good to research and utilize interesting circuits from outside sources, **you must not submit** these aspects as part of your final project design without crediting the source! To do so would amount to plagiarism.

**Dealing with Problems**

Although the vast majority of students in ECE110 are considerate, hard-working, intelligent young adults, some may have picked up bad habits. Others may make a serious error in judgment when under the pressures that a top institution like the University of Illinois may present. If you encounter a difficulty with a teammate or another student in the course that cannot be resolved, you have several actions of recourse. For example, you can ask to be paired with a different student within the first six weeks of the semester (or later, if the situation is more serious). Alternately, you might meet with your professor or an advisor in ECE where he/she can act as a moderator to help rectify the problems. In any case, it is better to address any issues as soon as they appear.

**Safety Considerations**

What are the hazards in an electrical engineering laboratory? Here, we explain how several (specific to ECE) may affect you.

**Electric shock**

Some of the instruments are capable of providing currents high enough to cause ventricular fibrillation of the heart (greater than 0.1 A through the heart). Fortunately, the lower voltages (< 10 V) provided by this equipment coupled with the typically-high resistance of the body to current flow makes this risk very low in ordinary conditions.

A greater concern is involved when plugging something into the 110-V, 60-Hz wall socket which is capable of providing much higher currents. Never remove the case of any electrical device or design your own system that draws power from a wall outlet without proper supervision. Do not assume that because you are training to be an engineer, that you are inherently trained to handle high-power devices.

Large capacitors (like those also found in large appliances like CRT televisions or microwave ovens) are also capable of providing high voltages and correspondingly-high current. Many other devices contain marginally-large capacitors, but are often discharged automatically by a “bleed resistor”. If you find yourself in the presence of a capacitor much thicker than your little finger, you should ask your lab instructor if it is safe to handle or use in your project.
Burns and Fire

While the power drawn from the sources in the lab are not typically a shock hazard, they can indirectly cause burns. Electrical devices are designed to dissipate a certain amount of electrical power without overheating. If a device is pushed beyond these limits by being wired incorrectly or being supplied with too much power, they can become very hot to the touch and often begin to burn and produce smoke. If you smell something burning and suspect it might be your circuit, quickly **disconnect the power from your circuit**. Be careful when disconnecting power from a circuit in this situation as the power source itself (a battery, perhaps) may be very hot. The best practice is to use a pencil or other non-conducting object to physically disconnect the power source. **DO NOT TOUCH** any part of the circuit as it may remain hot for some time, but rather **look** for wiring errors, melted plastic or visibly burnt devices. If you find none, have your TA present before you plug it back in.

The most common serious mistake in wiring is connecting directly connecting the positive and negative terminals of a battery (generally called a **short**) through a non-obvious path on the breadboard. Most desktop power supplies have protective circuits that will limit the current or disable the output when the positive and negative terminals are shorted. Batteries typically have no such protection and can supply enough current to burn up components or melt a breadboard. Shorting a battery not only can cause burns due to hot devices, but can even result in the battery bursting into flames. Our batteries should be augmented with a thermal fuse, but do not rely on it! Be sure to use care with batteries. **Transport or store batteries in a manner in which they cannot short.**

During the semester, several students may wish to take the opportunity to learn the basics of soldering. While most people recognize the inherent danger of the hot end of the soldering iron, they often fail to recognize how much of that heat is transferred to the solder joint and adjoining metal parts of the circuit and how long it may take for those parts to cool. The larger the volume of metal being joined, the longer it will take to heat and then cool the parts when soldering. **Allow at least three times as long to cool as it took to heat and solder the joint.**

Chemical Poisoning

Soldering is closely related to main source of chemical danger in the electrical engineering laboratory, lead (chemical element Pb). This heavy metal, when inhaled or ingested, is highly toxic and can lead to a multitude of problems affecting the nervous system and internal organs and can even cause death. Women who may be pregnant should remain especially mindful of the damage lead poisoning may cause to a developing fetus.

Many solders today are produced lead-free (but not necessarily toxin-free), but it is highly recommended that you learn proper soldering techniques that protect you from the ill-effects of potential toxins. Take precautions to **avoid breathing the fumes**.
when soldering and always wash your hands as soon as possible after soldering or handling soldered components. It should be evident that you should never bring food into any electrical engineering laboratories!

Cuts and Puncture Wounds
There are a few sharp objects in the lab that students are likely to handle. Carelessness around such sharp objects can lead to unnecessary injuries and possible infections.

The ends of the wires (often called leads and pronounced LEEDS) of the different components used such as resistors and integrated circuits are often sharp enough to puncture skin when enough pressure is applied. It is important to handle all components with care.

A wire stripper is a scissors-like device with notches cut into the blades to allow the experimenter to remove wire insulation without cutting the wire by mistake. Handle the wire strippers as you would scissors and be careful to keep your fingers away from the sharp cutting edges.