CS 421 Lecture 11: Code generation

Lecture outline

- Compiler structure
- Run-time environment
- Execution of static languages
- Code optimization why?
- Code generation
- Code optimization how?

Compiler structure

For traditional (static) languages:



Review: front end



Back end



 Intermediate representation (IR) is simpler to operate on than machine language

- Once we have the program in machine code, how do we run it?
 - Instructions executed directly on HW
 - OS calls?
 - Variables?
 - Function/procedure calls?
- Memory architecture
- Information about the program

Memory layout:



Stack structure:



Heap structure:



Code optimization – motivating example

Just to show effect of code optimization, here's a C program:

```
main () {
    int i, j, k;
    i = (j+1)*(k-1);
    printf("%d", I);
}
```

Code optimization – motivating example

Machine code produced by C compiler:

| leal | 4(%esp), %ecx |
|--|--|
| andl | \$-16, %esp |
| pushl | -4(%ecx) |
| pushl | %ebp |
| movl | %esp, %ebp |
| pushl | %ecx |
| subl | \$36, %esp |
| movl | -12(%ebp), %edx |
| addl | \$1, % edx |
| movl | -8(%ebp), %eax |
| subl | \$1, %eax |
| | |
| imull | %edx, %eax |
| imull movl | <pre>%edx, %eax %eax, -16(%ebp)</pre> |
| imull movl movl | <pre>%edx, %eax %eax, -16(%ebp) -16(%ebp), %eax</pre> |
| imull movl movl movl | <pre>%edx, %eax %eax, -16(%ebp) -16(%ebp), %eax %eax, 4(%esp)</pre> |
| imull movl movl movl movl | <pre>%edx, %eax %eax, -16(%ebp) -16(%ebp), %eax %eax, 4(%esp) \$.LC0, (%esp)</pre> |

Code optimization – motivating example

Machine code produced by C compiler with –O4:

| leal | 4(%esp), %ecx |
|-------|----------------|
| andl | \$-16, %esp |
| pushl | -4(%ecx) |
| addl | \$1, %eax |
| leal | -1(%eax), %edx |
| imull | %edx, %eax |
| Pushl | %ebp |
| Movl | %esp, %ebp |
| Pushl | %ecx |
| Subl | %20, %esp |
| movl | %eax, 4(%esp) |
| movl | \$.LCO, (%esp) |
| call | printf |

Code optimization

- How can we get there?
 - Go directly from AST+ST -> ML
 - What is the problem?
 - Use intermediate representation
- IR
 - Close enough to machine language that IR -> ML translation is easy
 - Abstracts over some details, platform-specific features, *etc.*

Translation to IR

- Different types of intermediate representations
 - Stack machine
 - 3-address instructions
 - 2-address instructions
 - Various graph structures showing control flow and data dependencies
- Consider translation to 3-address form:
 - [S] : Statement -> instruction list
 - [e] : Expression -> instruction list * variable
 - At this stage, we are not thinking about machine registers. Just give every value a location name.
 - In later stage, decide whether value will to in memory, in register, or on stack.

Expressions:

• $[x] (x is a variable) = (\varepsilon, x)$

Example 1

```
[n] = let t = newloc() in (t = n, t)
[x] = (ɛ, x)
[e1 + e2] = let (I<sub>1</sub>, t<sub>1</sub>) = [e1], (I<sub>2</sub>, t<sub>2</sub>) = [e2]
t<sub>3</sub> = newloc ()
in (I<sub>1</sub> , t<sub>3</sub>)
I<sub>2</sub>
t<sub>3</sub> = t<sub>1</sub> + t<sub>2</sub>
x + (10 * y)
```

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Example 2

- Statements
- Assignment
- Sequence (block)

If-then-else

```
[ if e then S1 else S2 ] =
    let (I, t) = [ e ]
        L1, L2, L3 = newlabels()
        in I
            CJUMP t, L1, L2
        L1: [ S1 ]
            JUMP L3
        L2: [ S2 ]
        L3:
```

While

```
[ while e do S1 ] =
    let (I, t) = [ e ]
        L1, L2, L3 = newlabels()
        in ??
```

While

```
[ while e do S1 ] =
    let (I, t) = [ e ]
        L1, L2, L3 = newlabels()
        in JUMP L2
        L1: [ S1 ]
        L2: I
        CJUMP t, L1, L3
        L3:
```

Procedure call

```
[ f(e1, ... , en) ] =
    let (I<sub>i</sub>, t<sub>i</sub>) = [ e<sub>i</sub> ] for all I
    in I<sub>1</sub>
    PUSH t<sub>1</sub>
    I<sub>2</sub>
    PUSH t<sub>2</sub>
    ...
    I<sub>n</sub>
    PUSH t<sub>n</sub>
    CALL f
```

What is left?

- Next class
 - Finish up statements
 - Switch
 - Break
 - Finish up expressions
 - Arrays
 - Booleans
 - Implementing code optimization