Locality



A time-honored technique to improve locality is tiling.

We will illustrate its benefits to reduce cache misses using two simple examples.



Transposing a matrix.

Consider the loop:

```
for ( i=0, i < n, i++) {
   for ( j=i+1, j < n, j++) {
      t=a[j,i]
      a[j,i]=a[i,j]
      a[i,j] = t
   }
}</pre>
```

Assume each cache line contains L array elements. If the cache had fewer than n cache lines, there would be one cache miss

- > on every iteration of the inner loop for each row of a accessed (for a total of n-i-1 for the whole loop j on iteration i) and
- > one cache miss for each line across rows $\left[\frac{(n-i-1)}{L}\right]$. Why ?

Therefore, number of cache misses would be bounded as follows:

$$\sum_{i=0}^{n-1} (n-i-1) + \left\lceil \frac{(n-i-1)}{L} \right\rceil > \sum_{i=0}^{n-1} (n-i-1) + \frac{(n-i-1)}{L} = n(n-1) (1+1/L) / 2$$

If, on the other hand, we tile the matrix transpose as follows:

```
for ( i=0, i < n, i+=s) { /* assume n multile of s */
    transpose(a[i:i+s-1,i:i+s-1])
    for ( j=i+s, j < n, j+=s) {
        transpose(a[i:i+s-1,j:j+s-1], a[j:j+s-1,i:i+s-1])
     }
}</pre>
```

where the function transpose transforms its parameter when there is only one parameter, otherwise transposes the two submatrices and exchanges them.

If the two submatrices fit in the cache, the number of cache misses will be only n^2/L .



Matrix multiplication.

Consider the loop

```
for ( i=1, i <= n, i++) {
   for ( j=1, j <= n, j++) {
      for ( k=1, k <= n, k++) {
         c[i,j]=a[i,k]*b[k,j]+c[i,j]
      }
   }
}</pre>
```

If the cache contains fewer than n/L lines, there will be one cache miss for every execution of the k loop will bring n misses due to b and n/L due to a. Each execution of the j loop will bring n/L misses due to c.

Total: $n^3 + n^3 / L + n^2 / L$



The middle product version of matrix matrix multiplication behaves better:

```
for ( i=1, i <= n, i++) {
   for ( k=1, k <= n, k++) {
      for ( j=1, j <= n, j++) {
         c[i,j]=a[i,k]*b[k,j]+c[i,j]
      }
   }
}</pre>
```

Under the same assumptions, we have that each iteration of the **j** loop brings n/L cache misses due to **c** and the same number due to **b**. Each iteration of the **k** loop brings additionally n/L misses due to **a**.

Total: $2n^3/L + n^2/L$



If we now tile matrix matrix multiplication, we get

Assumming that the three tiles fit in cache, we will have $2 s^2 / L$ misses for **matmul** due to **a** and **b** and additionally s^2 / L misses due to **c** for each iteration of the **j** loop.

Total: $(n/s)^{3} \cdot 2 s^{2}/L + (n/s)^{2} \cdot s^{2}/L = 2 n^{3}/(s L) + n^{2}/L$