# CS411 Database Systems

14: Concurrency Control

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#### Announcements

- Homework 5 due on Dec 1
- Graduate project is due on Dec 1
- Project stage 5 is due on Dec 3
- No office hour next Friday (Nov. 26)

# Undo/Redo Logging

# Redo/undo logs save both before-images and after-images.

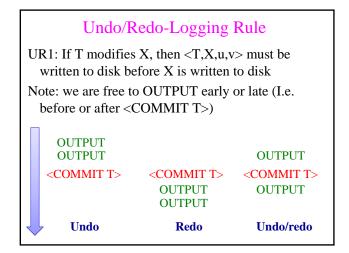
```
<START T>
```

<COMMIT T>

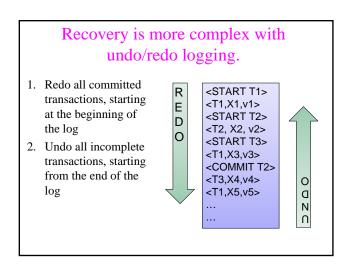
<ABORT T>

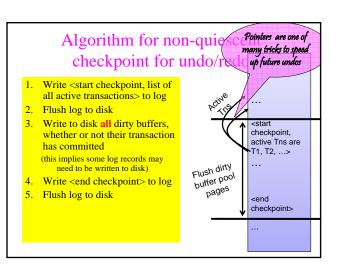
<T, X, old\_v, new\_v>

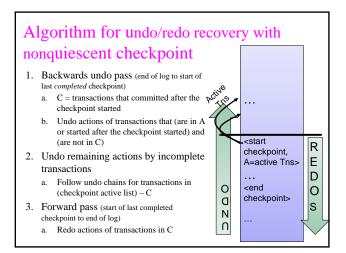
 T has written element X; its old value was old\_v, and its new value is new\_v

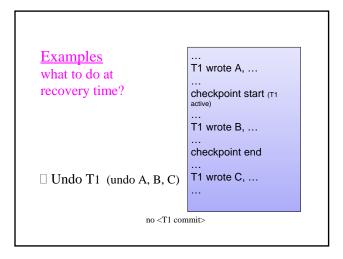


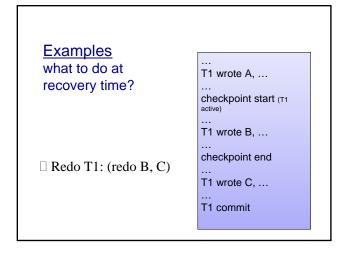
Action	T	Mem A	Mem B	Disk A	Disk B	Log (memory)	Log (disk)
						<start t=""></start>	
READ(A,t)	8	8		8	8		
t := t*2	16	8		8	8		
WRITE(A,t)	16	16		8	8	<t,a,8,16></t,a,8,16>	
READ(B,t)	8	16	8	8	8		
t := t*2	16	16	8	8	8		
WRITE(B,t)	16	16	16	8	8	<t,b,8,16></t,b,8,16>	
FLUSH LOG							<start t=""> <t, 16="" 8,="" a,=""> <t, 16="" 8,="" b,=""></t,></t,></start>
OUTPUT(A	16	16	16	16	8		
						<commit t=""></commit>	
FLUSH LOG							<commit t=""></commit>
OUTPUT(B	16	16	16	16	16		



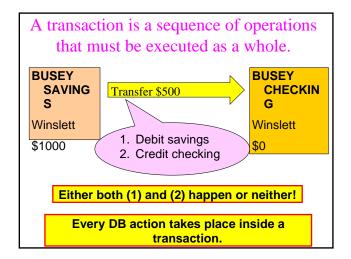


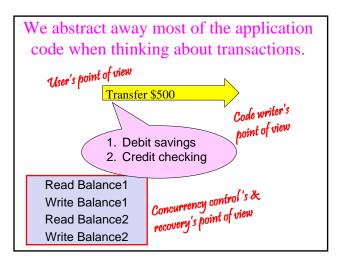


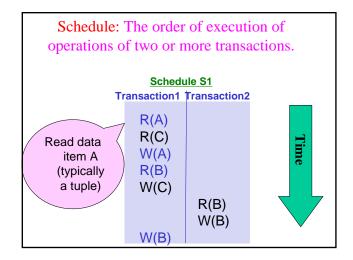


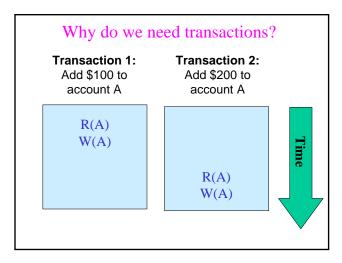


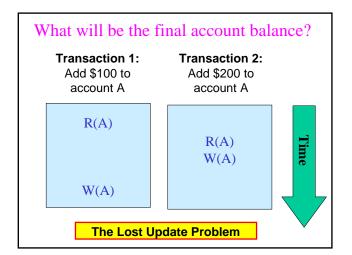
Concurrency Control

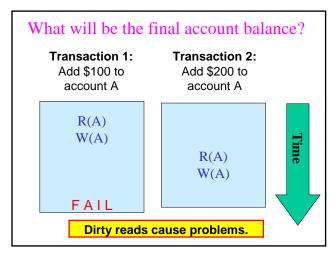












# Abort or roll back are the official words for "fail".

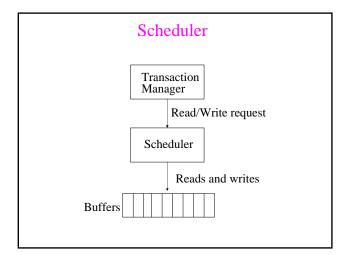
#### Commit

All your writes will definitely absolutely be recorded and will not be undone, and all the values you read are committed too.

#### Abort/rollback

Undo all of your writes!

The concurrent execution of transactions must be such that each transaction appears to execute in isolation.

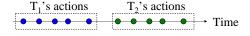


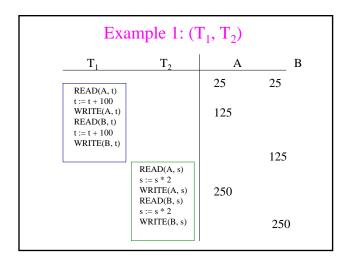
### Schedule

- Time-ordered sequence of the important actions taken by one or more transactions
- Consider only the READ and WRITE actions, and their orders; ignore the INPUT and OUTPUT actions
  - An element in a buffer is accessed by multiple transactions

#### Serial Schedule

- If any action of transaction T<sub>1</sub> precedes any action of T<sub>2</sub>, then all action of T<sub>1</sub> precede all action of T<sub>2</sub>
- The correctness principle tells us that every serial schedule will preserve consistency of the database state





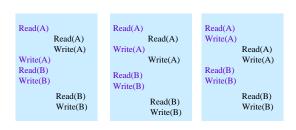
Example 2: (T <sub>2</sub> , T <sub>1</sub> )								
	T <sub>1</sub>	$T_2$	A	В				
		READ(A, s)	25	25				
		s := s * 2 WRITE(A, s) READ(B, s) s := s * 2	50					
		WRITE(B, s)		50				
	READ(A, t) t := t + 100 WRITE(A, t) READ(B, t)		150					
	t := t + 100 WRITE(B, t)			150				

## Serial Schedule is Not Necessarily Desirable

- Improved throughput
  - I/O activity can be done in parallel with processing at CPU
- Reduced average waiting time
  - If transactions run serially, a short transaction may have to wait for a preceding long transaction to complete

A schedule is serializable if it is guaranteed to give the same final result as some serial schedule.

Which of these are serializable?



#### Notation for Transactions and Schedules

- We do not consider the details of local computation steps such as t := t + 100
- Only the reads and writes matter
- Action:  $r_i(X)$  or  $w_i(X)$
- Transaction Ti: a sequence of actions with subscript i
- Schedule S: a sequence of actions from a set of transactions T

## Examples

- T1: r<sub>1</sub>(A); w<sub>1</sub>(A); r<sub>1</sub>(B); w<sub>1</sub>(B);
- T2: r<sub>2</sub>(A); w<sub>2</sub>(A); r<sub>2</sub>(B); w<sub>2</sub>(B);
- $\bullet \ \ S{:}\ r_1(A); \, w_1(A); \, r_2(A); \, w_2(A); \, r_1(B); \, w_1(B); \, r_2(B); \, w_2(B);$

## Conflict-Serializability

- Commercial systems generally support *conflict-serializability* 
  - Stronger notion than serializability
- Based on the idea of a conflict
- Turn a given schedule to a serial one by make as many nonconflicting swaps as we wish

#### Conflicts

 A pair of consecutive actions in a schedule such that, if their order is interchanged, then the behavior of at least one of the transactions involved can change

## **Conflicting Swaps**

- Two actions of the same transaction
  - E.g.,  $r_i(X)$ ;  $w_i(Y)$
- Two writes of the same database element
  - E.g.,  $w_{i}(X)$ ;  $w_{i}(X)$
- A read and a write of the same database element
  - E.g.,  $r_i(X)$ ;  $w_i(X)$

## Nonconflicting swaps

- Any two actions of different transactions may be swapped unless:
  - They involve the same database element, and
  - At least one is a write
- Examples:
  - 1.  $r_i(X); r_i(Y)$
  - 2.  $r_i(X)$ ;  $w_i(Y)$  if X!=Y
  - 3.  $w_i(X)$ ;  $r_i(Y)$  if X != Y
  - 4.  $w_i(X)$ ;  $w_i(Y)$  if X != Y

#### Conflict-serializable

- Two schedules are *conflict-equivalent* if they can be turned one into the other by a sequence of nonconflicting swaps of adjacent actions
- A schedule is *conflict-serializable* if it is conflictequivalent to a serial schedule
- Easy to check whether a schedule is conflictserializable by examining a precedence graph

# Example

$$\begin{split} &r_1(A);\,w_1(A);\,r_2(A);\,w_2(A);\,r_1(B);\,w_1(B);\,r_2(B);\,w_2(B);\\ &r_1(A);\,w_1(A);\,r_2(A);\,r_1(B);\,w_2(A);\,w_1(B);\,r_2(B);\,w_2(B);\\ &r_1(A);\,w_1(A);\,r_1(B);\,r_2(A);\,w_2(A);\,w_1(B);\,r_2(B);\,w_2(B);\\ &r_1(A);\,w_1(A);\,r_1(B);\,r_2(A);\,w_1(B);\,w_2(A);\,r_2(B);\,w_2(B);\\ &r_1(A);\,w_1(A);\,r_1(B);\,w_1(B);\,r_2(A);\,w_2(A);\,r_2(B);\,w_2(B);\\ \end{split}$$

## Test for Conflict-Serializability

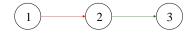
- Can decide whether or not a schedule S is conflict-serializable
- Ideas:
  - when there are conflicting actions that appear anywhere in S, the transactions performing those actions must appear in the same order in any conflictequivalent serial schedule
  - Summarize those conflicting actions in a precedence graph

## Precedence Graphs

- T<sub>1</sub> takes precedence over T<sub>2</sub> (T<sub>1</sub> <<sub>S</sub> T<sub>2</sub>), if there are actions A<sub>1</sub> of T<sub>1</sub> and A<sub>2</sub> of T<sub>2</sub>, s.t.
  - A<sub>1</sub> is ahead of A<sub>2</sub> in S
  - Both  $A_1$  and  $A_2$  involve the same database element
  - At least one of  $A_1$  and  $A_2$  is a written action
- Construct a precedence graph and ask if there are any cycles

# Example

 $S\colon r_2(A);\, r_1(B);\, w_2(A);\, r_3(A);\, w_1(B);\, \underline{w_3}(A);\, r_2(B);\, \underline{w_2}(B);$ 



S':  $r_1(B)$ ;  $w_1(B)$ ;  $r_2(A)$ ;  $w_2(A)$ ;  $r_2(B)$ ;  $w_2(B)$ ;  $r_3(A)$ ;  $w_3(A)$ ;

# Example

 $S_1$ :  $r_2(A)$ ;  $r_1(B)$ ;  $w_2(A)$ ;  $r_2(B)$ ;  $r_3(A)$ ;  $w_1(B)$ ;  $w_3(A)$ ;  $w_2(B)$ ;

