Pre-lecture brain teaser

A boolean expression is in disjunctive normal form if it consists of the union of clauses where each clause is composed of the intersection of literals. For example:

$$(\overline{X_1} \wedge X_3 \wedge X_4) \vee (X_2 \wedge \overline{X_3} \wedge X_4) \tag{1}$$

Imagine we have a problem: DNF-SAT, where given a DNF formula, we want to know if there is a satisfying assignment. We know two things:

- Finding a satisfying assignment for a DNF formula takes polynomial time.
- We can rewrite any CNF formula as a DNF formula.

Hence I do the smart thing and say since CNF-SAT \leq_P DNF-SAT, then CNF-SAT \in NP.

Am I correct?

CS/ECE-374: Lecture 26 - NP-Complete reductions

Lecturer: Nickvash Kani

Chat moderator: Samir Khan

April 27, 2021

University of Illinois at Urbana-Champaign

Pre-lecture brain teaser

SAT: (NF: (x, Vx2 Vx3) 1 (x, Vx2 Vx3)... A boolean expression is in disjunctive normal form if it consists of the union of clauses where each clause is composed of the intersection of literals. For example: $(\overline{X_1} \wedge X_3 \wedge X_4) \vee (X_2 \wedge \overline{X_3} \wedge X_4)$

Imagine we have a problem: DNF-SAT, where given a DNF formula, we want to know if there is a satisfying assignment. We know two things:

· Finding a satisfying assignment for a DNF formula takes

- polynomial time.
- · We can rewrite any CNF formula as a DNF formula.

Hence I do the smart thing and say since CNF-SAT $\leq \frac{1}{2}$ DNF-SAT, OUF f(+)= (KIVA) then CNF-SAT $\in \mathbb{N}^{\mathbb{N}}$. DNP (10) = x, x2+ xxx+ x, x2

Am I correct?

Today

NP-Completeness of two problems:

- Hamiltonian Cycle
- 3-Color

Important: understanding the problems and that they are hard.

Proofs and reductions will be sketchy and mainly to give a 🦯 flavor

NP-Complete > DP-hand (harden than all NP problems)

ENP > Write poly time certifier C(s, +)

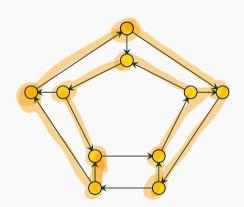
p justome solution

Reduction from 3SAT to Hamiltonian Cycle

Directed Hamiltonian Cycle

Input Given a directed graph G = (V, E) with n vertices **Goal** Does G have a Hamiltonian cycle?

• 2- A Hamiltonian cycle is a cycle in the graph that visits every vertex in *G* exactly once

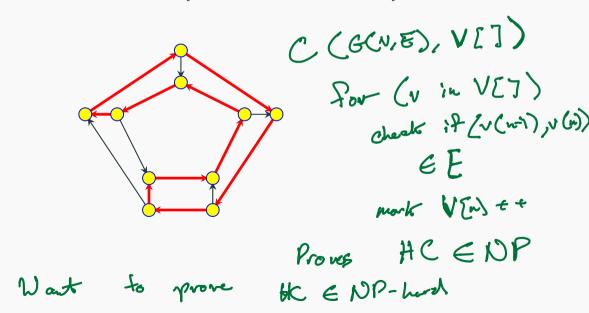


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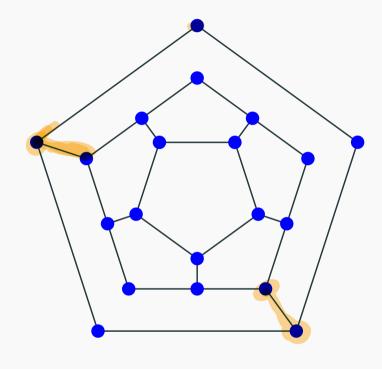
Directed Hamiltonian Cycle

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Is the following graph Hamiltonianan?



a Yes.

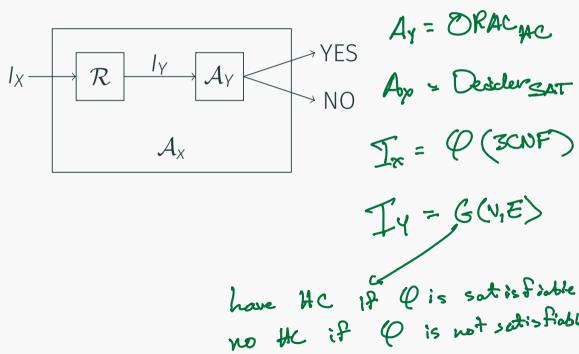
b No.

Directed Hamiltonian Cycle is NP-Complete

- Directed Hamiltonian Cycle is in NP: exercise
- Hardness: We will show $3-SAT \leq_P Directed Hamiltonian Cycle NPLml$

Directed Hamiltonian Cycle is NP-Complete

- Directed Hamiltonian Cycle is in NP: exercise
- Hardness: We will show
 3-SAT ≤_P Directed Hamiltonian Cycle



Reduction

Given 3-SAT formula φ create a graph G_{φ} such that

- G_{φ} has a Hamiltonian cycle if and only if φ is satisfiable
- · G_{arphi} should be constructible from arphi by a polynomial time algorithm ${\mathcal A}$

Notation: φ has n variables x_1, x_2, \ldots, x_n and m clauses C_1, C_2, \ldots, C_m .

Reduction: First Ideas

- Construct graph with 2ⁿ Hamiltonian cycles, where each cycle corresponds to some boolean assignment.
- Then add more graph structure to encode constraints on assignments imposed by the clauses.

Need to create a graph from any arbitrary boolean assignment.

Consider the expression:



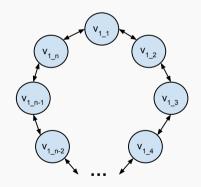
Need to create a graph from any arbitrary boolean assignment. Consider the expression:

$$f(x_1) = 1$$

$$x = 0$$

$$x_1 = 1$$
(3)

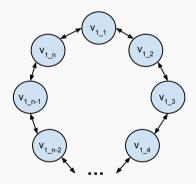
We create a cyclic graph that always has a hamiltonian:



Need to create a graph from any arbitrary boolean assignment. Consider the expression:

$$f(x_1) = 1 \tag{3}$$

We create a cyclic graph that always has a hamiltonian:

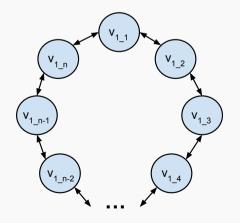


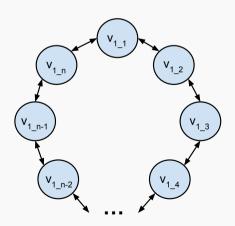
But how do we encode the variable?

Need to create a graph from any arbitrary boolean assignment. Consider the expression:

$$f(x_1) = 1 \tag{4}$$

Maybe we can encode the variable x_1 in terms of the cycle direction:

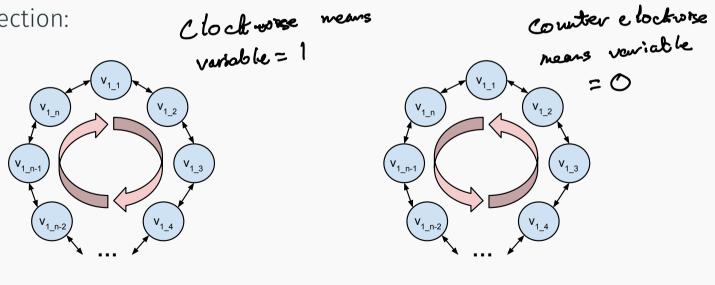




Need to create a graph from any arbitrary boolean assignment. Consider the expression:

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If
$$x_1 = 1$$

If
$$x_1 = 0$$

How do we encode multiple variables?

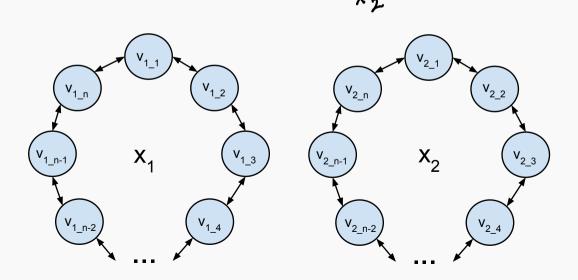
$$f(x_1, x_2) = 1$$
 (5)

Maybe two circles? Now we need to connect them so that we have a single hamiltonian path

How do we encode multiple variables?

$$f(x_1, x_2) = 1 (5)$$

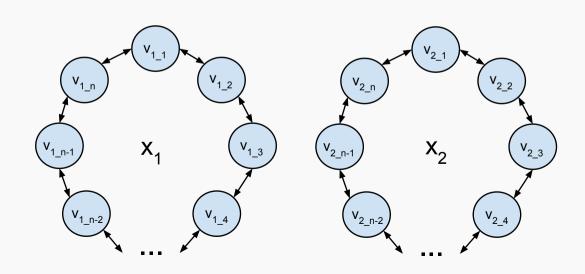
Maybe two circles? Now we need to connect them so that we have a single hamiltonian path



How do we encode multiple variables?

$$f(x_1, x_2) = 1 (6)$$

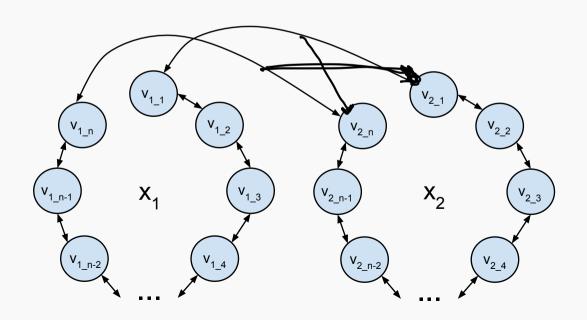
Now we need to connect them so that we have a single hamiltonian path



How do we encode multiple variables?

$$f(x_1, x_2) = 1 (6)$$

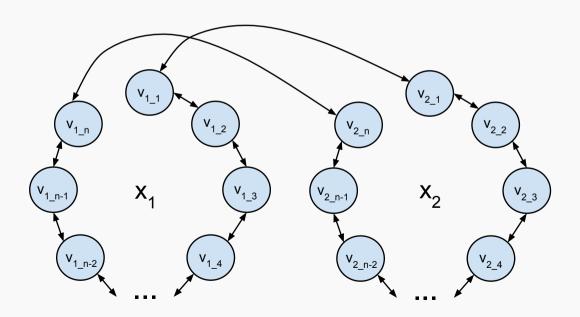
Now we need to connect them so that we have a single hamiltonian path $x_1x_2 = 0$



How do we encode multiple variables?

$$f(x_1, x_2) = 1 (7)$$

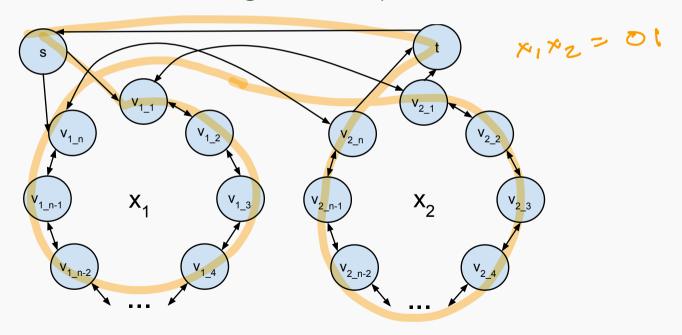
Would be nice to have a single start/stop node.



How do we encode multiple variables?

$$f(x_1, x_2) = 1 (7)$$

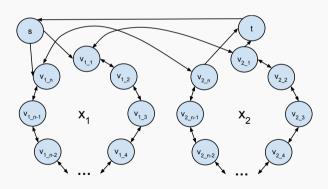
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How do we encode multiple variables?

$$f(x_1, x_2) = 1 (8)$$

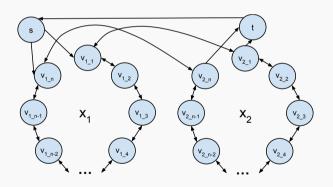
Getting a bit messy. Let's reorganize:

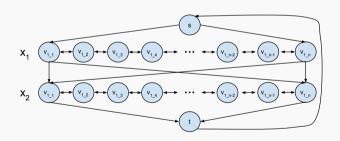


How do we encode multiple variables?

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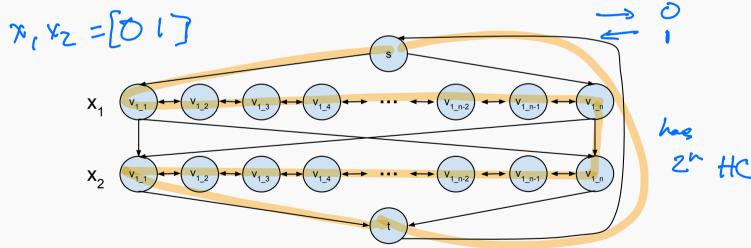




How do we encode multiple variables?

$$f(x_1, x_2) = 1 (9)$$

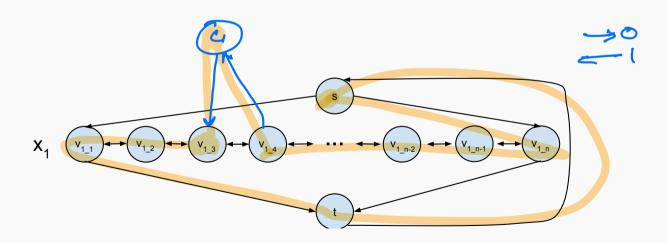
This is how we encode variable assignments in a variable loop!



How do we handle clauses?

$$f(x_1) = \begin{pmatrix} c_1 \\ x_1 \end{pmatrix} \qquad c_1 = 1 \tag{10}$$

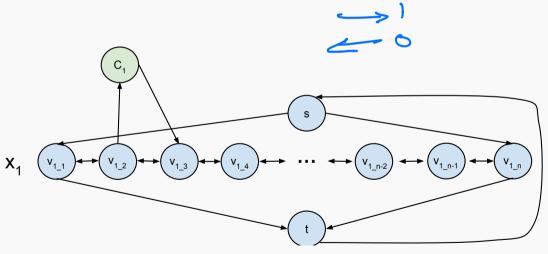
Lets go back to our one variable graph:



How do we handle clauses?

$$f(x_1) = x_1 \tag{11}$$

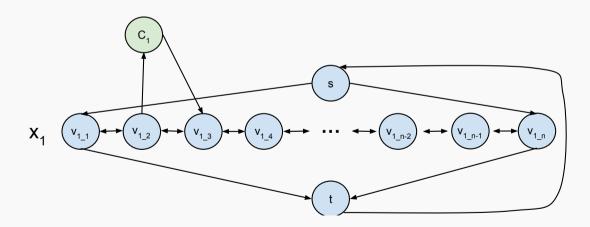
Add node for clause:



How do we handle clauses?

$$f(x_1, x_2) = (x_1 \vee \overline{x_2})$$
 (12)

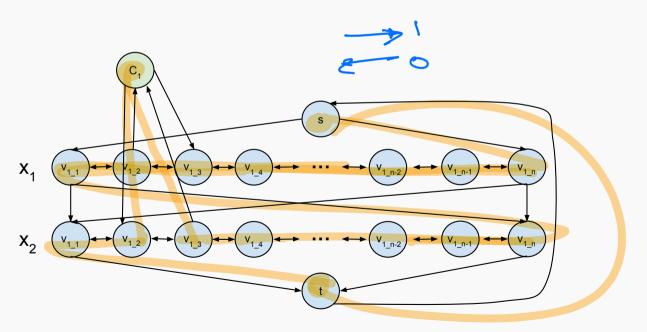
What do we do if the clause has two literals:



How do we handle clauses?

$$f(x_1, x_2) = (x_1 \vee \overline{x_2}) \qquad [0, 0] \qquad (12)$$

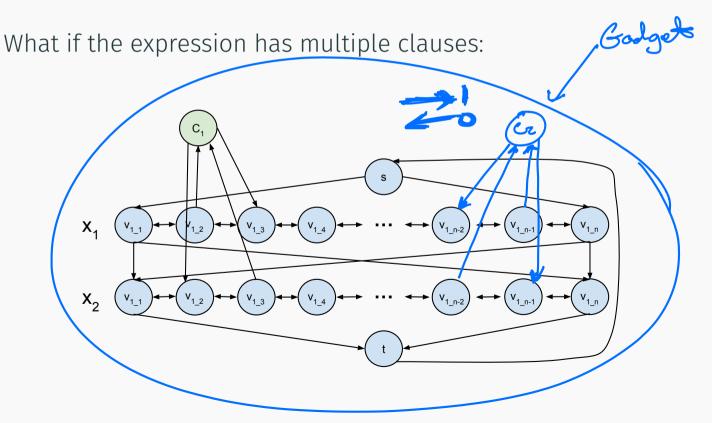
What do we do if the clause has two literals:



f(x1,x2) = 1

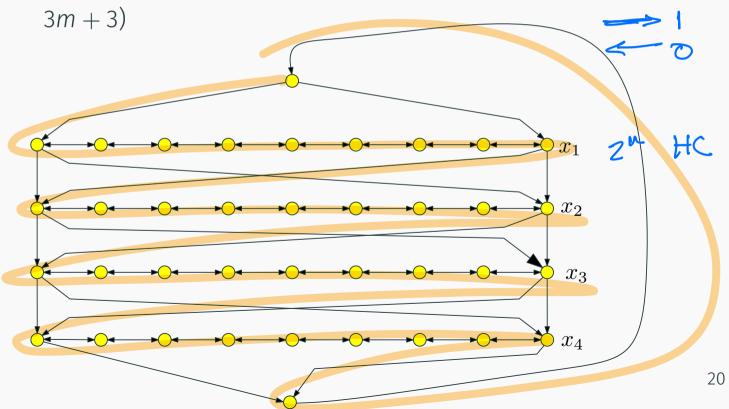
How do we handle clauses?

$$f(x_1, x_2) = (x_1 \vee \overline{x_2}) \wedge (\overline{x_1} \vee x_2)$$
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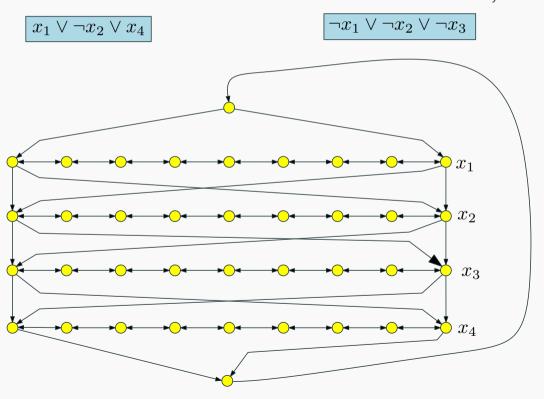
The Reduction: Review I

- Traverse path i from left to right iff x_i is set to true
- Each path has 3(m+1) nodes where m is number of clauses in φ ; nodes numbered from left to right (1 to



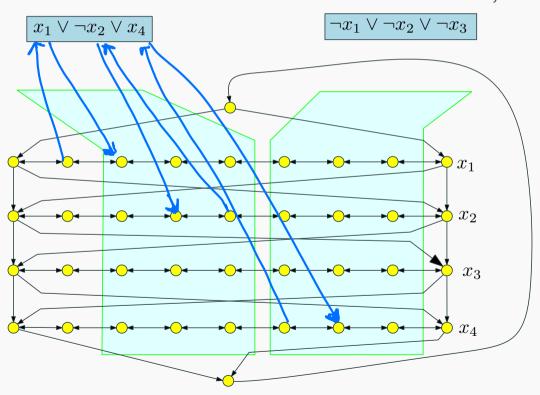
The Reduction algorithm: Review II

Add vertex c_j for clause C_j . c_j has edge from vertex 3j and to vertex 3j + 1 on path i if x_i appears in clause C_j , and has edge from vertex 3j + 1 and to vertex 3j if $\neg x_i$ appears in C_j .



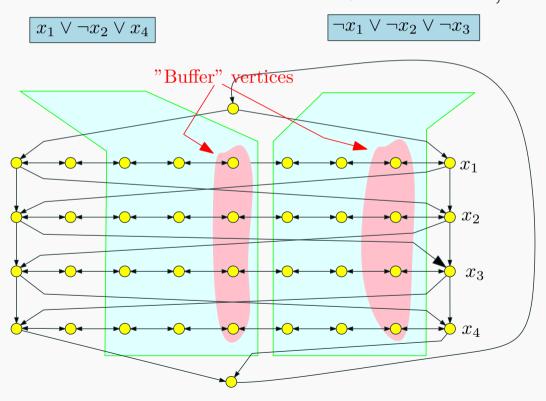
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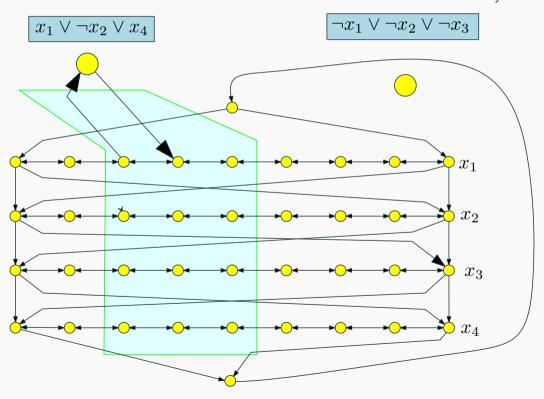
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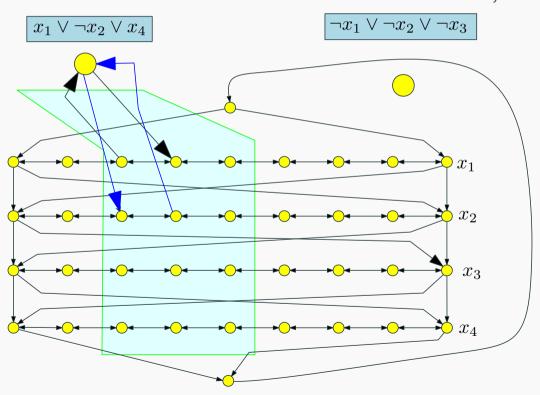


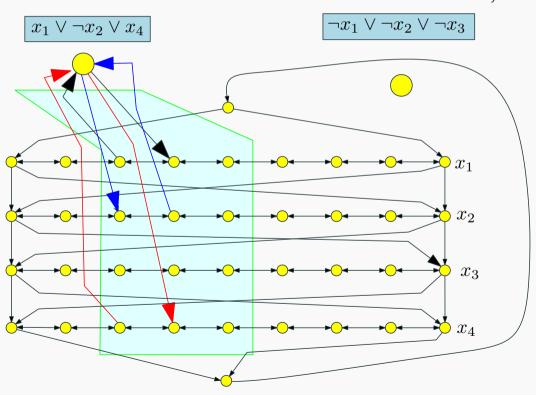
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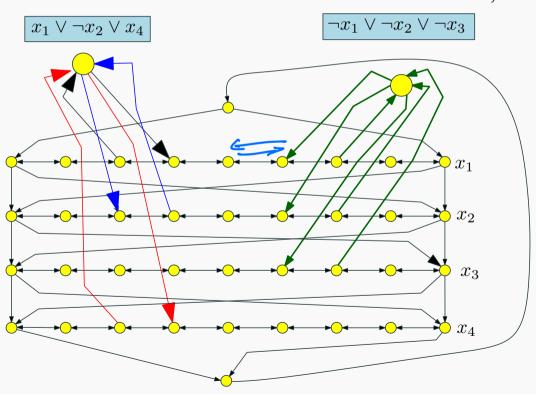
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Correctness Proof

Theorem

 φ has a satisfying assignment iff G_{φ} has a Hamiltonian cycle.

Based on proving following two lemmas.

Lemma

If φ has a satisfying assignment then G_{φ} has a Hamilton cycle.

Lemma

If G_{φ} has a Hamilton cycle then φ has a satisfying assignment.

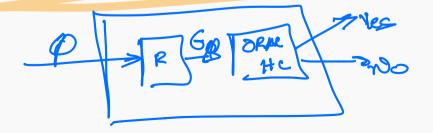
Satisfying assignment → Hamiltonian Cycle

Lemma

If φ has a satisfying assignment then G_{φ} has a Hamilton cycle.

Proof.

- \Rightarrow Let a be the satisfying assignment for φ . Define Hamiltonian cycle as follows
 - If $a(x_i) = 1$ then traverse path i from left to right
 - If $a(x_i) = 0$ then traverse path i from right to left
 - For each clause, path of at least one variable is in the "right" direction to splice in the node corresponding to clause



Hamiltonian Cycle → Satisfying assignment

Suppose Π is a Hamiltonian cycle in G_{φ}

Definition

We say Π is canonical if for each clause vertex c_j the edge of Π entering c_j and edge of Π leaving c_j are from the same path corresponding to some variable x_i . Otherwise Π is non-canonical or emphcheating.

Hamiltonian Cycle → Satisfying assignment

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Lemma

Every Hamilton cycle in G_{φ} is canonical.

Proof of Lemma

Lemma

Every Hamilton cycle in G_{φ} is canonical.

- If Π enters c_j (vertex for clause C_j) from vertex 3j on path i then it must leave the clause vertex on edge to 3j + 1 on the same path i
 - If not, then only unvisited neighbor of 3j + 1 on path i is 3j + 2
 - Thus, we don't have two unvisited neighbors (one to enter from, and the other to leave) to have a Hamiltonian Cycle
- Similarly, if Π enters c_j from vertex 3j + 1 on path i then it must leave the clause vertex c_i on edge to 3j on path i

Hamiltonian Cycle ->> Satisfying assignment (contd)

Lemma

Any canonical Hamilton cycle in G_{φ} corresponds to a satisfying truth assignment to φ .

Consider a canonical Hamilton cycle Π .

- For every clause vertex c_j , vertices visited immediately before and after c_j are connected by an edge on same path corresponding to some variable x_i
- We can remove c_j from cycle, and get Hamiltonian cycle in $G-c_i$
- Hamiltonian cycle from Π in $G \{c_1, \dots c_m\}$ traverses each path in only one direction, which determines truth assignment
- \cdot Easy to verify that this truth assignment satisfies arphi

graph

Hamiltonian cycle in undirected

Hamiltonian Cycle in *Undirected* Graphs

Problem

Input Given undirected graph G = (V, E)

Goal Does G have a Hamiltonian cycle? That is, is there a cycle that visits every vertex exactly one (except start and end vertex)?

Directed HC

Indiverted HC

We've

proved thirested-HC = NP-complete

D-G JR UG JUN-OSP NO DIR-HC-Oecider

NP-Completeness

Theorem Hamiltonian cycle problem for undirected graphs is NP-Complete.

Proof.

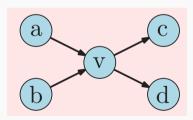
- The problem is in NP; proof left as exercise.
- Hardness proved by reducing Directed Hamiltonian Cycle to this problem

Goal: Given directed graph *G*, need to construct undirected graph *G'* such that *G* has Hamiltonian Path iff *G'* has Hamiltonian path

Reduction

•

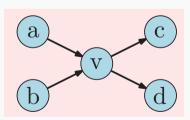
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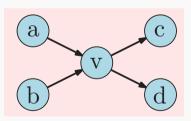
- Replace each vertex v by 3 vertices: v_{in} , v, and v_{out}
- .



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Reduction

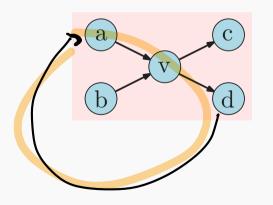
- Replace each vertex v by 3 vertices: v_{in} , v, and v_{out}
- A directed edge (a, b) is replaced by edge (a_{out}, b_{in})

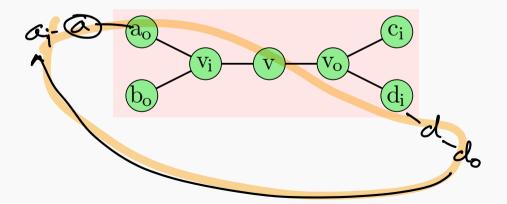


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Reduction

- Replace each vertex v by 3 vertices: v_{in} , v, and v_{out}
- A directed edge (a, b) is replaced by edge (a_{out}, b_{in})





Reduction: Wrapup

- The reduction is polynomial time (exercise)
- The reduction is correct (exercise)

Hamiltonian Path

Input Given a graph G = (V, E) with n vertices **Goal** Does G have a Hamiltonian path?

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Theorem
Directed Hamiltonian Path and Undirected Hamiltonian Path
are NP-Complete.

Easy to modify the reduction from **3-SAT** to **Halitonian Cycle** or do a reduction from **Halitonian Cycle**

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Easy to modify the reduction from **3-SAT** to **Halitonian Cycle** or do a reduction from **Halitonian Cycle**

Implies that Longest Simple Path in a graph is NP-Complete.

NP-Completeness of Graph Coloring

Graph Coloring

Problem: Graph Coloring

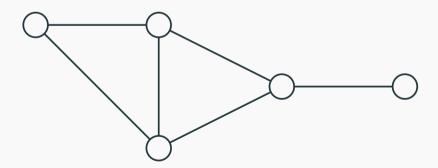
Instance: G = (V, E): Undirected graph, integer k. **Question:** Can the vertices of the graph be colored using k colors so that vertices connected by an edge do not get the same color?

Graph 3-Coloring

Problem: 3 Coloring

Instance: G = (V, E): Undirected graph.

Question: Can the vertices of the graph be colored using 3 colors so that vertices connected by an edge do not get the same color?



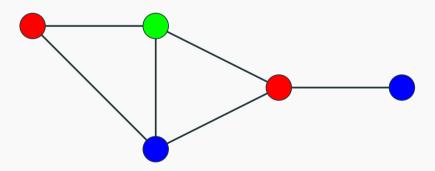
33

Graph 3-Coloring

Problem: 3 Coloring

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Question: Can the vertices of the graph be colored using 3 colors so that vertices connected by an edge do not get the same color?



33

Graph Coloring

Observation: If G is colored with k colors then each color class (nodes of same color) form an independent set in G. Thus, G can be partitioned into k independent sets iff G is k-colorable.

Graph 2-Coloring can be decided in polynomial time.

G is 2-colorable iff *G* is bipartite! There is a linear time algorithm to check if *G* is bipartite using Breadth-first-Search

Problems related to graph coloring

Graph Coloring and Register Allocation

Register Allocation

Assign variables to (at most) *k* registers such that variables needed at the same time are not assigned to the same register

Interference Graph

Vertices are variables, and there is an edge between two vertices, if the two variables are "live" at the same time.

Observations

- [Chaitin] Register allocation problem is equivalent to coloring the interference graph with *k* colors
- Moreover, 3-COLOR $\leq_P k$ Register Allocation, for any $k \geq 3$

Class Room Scheduling

Given *n* classes and their meeting times, are *k* rooms sufficient?

Reduce to Graph k-Coloring problem

Create graph G

- a node v_i for each class i
- an edge between v_i and v_j if classes i and j conflict

Exercise: G is k-colorable iff k rooms are sufficient

Frequency Assignments in Cellular Networks

Cellular telephone systems that use Frequency Division Multiple Access (FDMA) (example: GSM in Europe and Asia and AT&T in USA)

- Breakup a frequency range [a,b] into disjoint bands of frequencies $[a_0,b_0],[a_1,b_1],\ldots,[a_k,b_k]$
- · Each cell phone tower (simplifying) gets one band
- Constraint: nearby towers cannot be assigned same band, otherwise signals will interference

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- · Each cell phone tower (simplifying) gets one band
- Constraint: nearby towers cannot be assigned same band, otherwise signals will interference

Problem: given *k* bands and some region with *n* towers, is there a way to assign the bands to avoid interference?

Can reduce to *k*-coloring by creating intereference/conflict graph on towers.

Showing hardness of 3 COLORING

3-Coloring is NP-Complete

- 3-Coloring is in NP.
 - Non-deterministically guess a 3-coloring for each node
 - Check if for each edge (u, v), the color of u is different from that of v.
- Hardness: We will show 3-SAT \leq_P 3-Coloring.

Reduction Idea

Start with **3SAT** formula (i.e., 3CNF formula) φ with n variables x_1, \ldots, x_n and m clauses C_1, \ldots, C_m . Create graph G_{φ} such that G_{φ} is 3-colorable iff φ is satisfiable

- need to establish truth assignment for x_1, \ldots, x_n via colors for some nodes in G_{φ} .
- · create triangle with node True, False, Base
- for each variable x_i two nodes v_i and $\bar{v_i}$ connected in a triangle with common Base
- If graph is 3-colored, either v_i or $\bar{v_i}$ gets the same color as True. Interpret this as a truth assignment to v_i
- Need to add constraints to ensure clauses are satisfied (next phase)

Reduction Idea I - Simple 3-color gadget

We want to create a gadget that:

- Is 3 colorable if at least one of the literals is true
- Not 3-colorable if none of the literals are true

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Let's start off with the simplest SAT we cna think of:

$$f(x_1, x_2) = (x_1 \lor x_2) \tag{14}$$

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Let's start off with the simplest SAT we cna think of:

$$f(x_1, x_2) = (x_1 \lor x_2) \tag{14}$$

Assume green=true and red=false,

We want to create a gadget that:

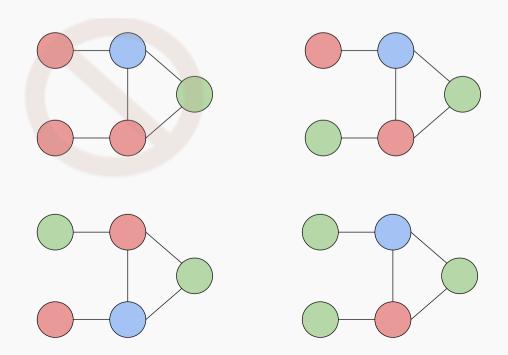
- Is 3 colorable if at least one of the literals is true
- Not 3-colorable if none of the literals are true

Let's try some stuff:

We want to create a gadget that:

- Is 3 colorable if at least one of the literals is true
- Not 3-colorable if none of the literals are true

Seems to work:



We want to create a gadget that:

- Is 3 colorable if at least one of the literals is true
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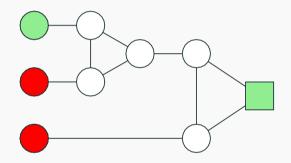
How do we do the same thing for 3 variables?:

$$f(x_1, x_2, x_3) = (x_1 \lor x_2 \lor x_3) \tag{15}$$

Assume green=true and red=false,

3 color this gadget II

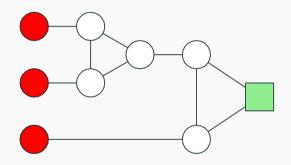
You are given three colors: red, green and blue. Can the following graph be three colored in a valid way (assuming that some of the nodes are already colored as indicated).



- a Yes.
- b No.

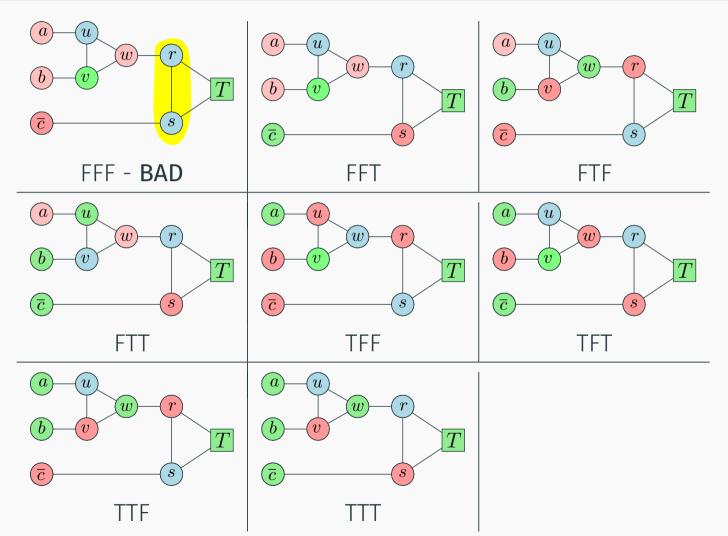
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- a Yes.
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3-coloring of the clause gadget

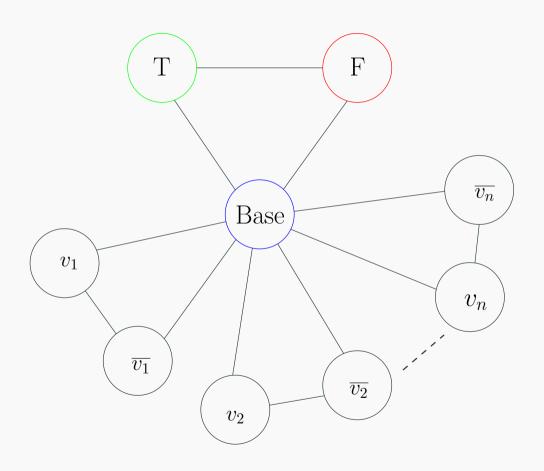


Reduction Idea II - Literal Assignment I

Next we need a gadget that assigns literals. Our previously constructed gadget assumes:

- · All literals are either red or green.
- Need to limit graph so only x_1 or $\overline{x_1}$ is green. Other must be red

Reduction Idea II - Literal Assignment II

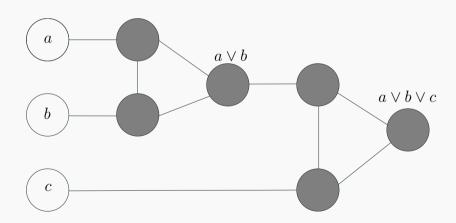


Review Clause Satisfiability Gadget

For each clause $C_i = (a \lor b \lor c)$, create a small gadget graph

- gadget graph connects to nodes corresponding to a, b, c
- needs to implement OR

OR-gadget-graph:



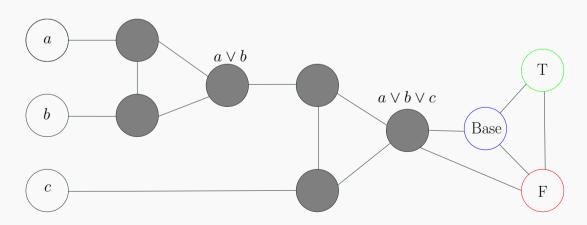
OR-Gadget Graph

Property: if a, b, c are colored False in a 3-coloring then output node of OR-gadget has to be colored False.

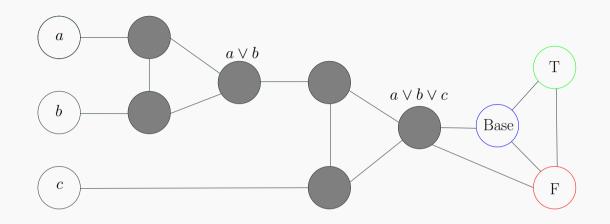
Property: if one of *a*, *b*, *c* is colored True then OR-gadget can be 3-colored such that output node of OR-gadget is colored True.

Reduction

- · create triangle with nodes True, False, Base
- for each variable x_i two nodes v_i and $\bar{v_i}$ connected in a triangle with common Base
- for each clause $C_j = (a \lor b \lor c)$, add OR-gadget graph with input nodes a, b, c and connect output node of gadget to both False and Base



Reduction

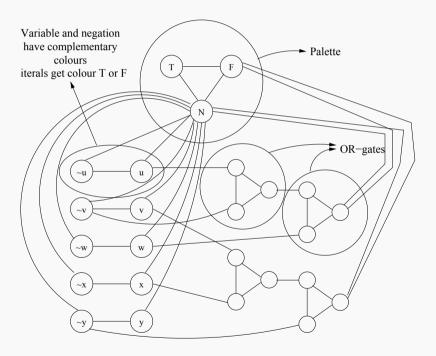


Lemma

No legal 3-coloring of above graph (with coloring of nodes T, F, B fixed) in which a, b, c are colored False. If any of a, b, c are colored True then there is a legal 3-coloring of above graph.

Reduction Outline

Example
$$\varphi = (u \lor \neg v \lor w) \land (v \lor x \lor \neg y)$$



 φ is satisfiable implies G_{φ} is 3-colorable

• if x_i is assigned True, color v_i True and $\bar{v_i}$ False

 φ is satisfiable implies G_{φ} is 3-colorable

- if x_i is assigned True, color v_i True and $\bar{v_i}$ False
- for each clause $C_j = (a \lor b \lor c)$ at least one of a, b, c is colored True. OR-gadget for C_j can be 3-colored such that output is True.

 φ is satisfiable implies G_{φ} is 3-colorable

- if x_i is assigned True, color v_i True and $\bar{v_i}$ False
- for each clause $C_j = (a \lor b \lor c)$ at least one of a, b, c is colored True. OR-gadget for C_j can be 3-colored such that output is True.

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 G_{φ} is 3-colorable implies φ is satisfiable

 if v_i is colored True then set x_i to be True, this is a legal truth assignment

 φ is satisfiable implies G_{φ} is 3-colorable

- if x_i is assigned True, color v_i True and $\bar{v_i}$ False
- for each clause $C_j = (a \lor b \lor c)$ at least one of a, b, c is colored True. OR-gadget for C_j can be 3-colored such that output is True.

 G_{φ} is 3-colorable implies φ is satisfiable

- if v_i is colored True then set x_i to be True, this is a legal truth assignment
- consider any clause $C_j = (a \lor b \lor c)$. it cannot be that all a, b, c are False. If so, output of OR-gadget for C_j has to be colored False but output is connected to Base and False!

Graph generated in reduction from 3SAT to 3COLOR

