

ECE 425
Course Information

Fall 2011

Martin D. F. Wong

Class Meetings

MWF, 8:00AM - 8:50AM, 245 Everitt Lab

Instructor

Prof. Martin D. F. Wong (mdfwong@illinois.edu)
Office hour: Tuesday 9:00AM - 10:00AM, CSL 409
Phone: 244-1729

Teaching Assistant

Yuelin Du (du6@illinois.edu)
Hongbo Zhang (hzhang27@illinois.edu)
Office hours: Tue 4PM - 5PM, Thu 5PM - 6PM, 252 Everitt (EWS lab)

Text Book

Weste and Harris, CMOS VLSI Design (4th edition)

Other Reference Books

Irwin and Kerns, *Essentials of Electrical and Computer Engineering*
Rabaey, Chandrakasan, and Nikolic, *Digital Integrated Circuits: a Design perspective*
De Micheli, *Synthesis and Optimization of Digital Circuits*
Sherwani, *Algorithms for VLSI Physical Design Automation*

Grading

Credit is given for

- Homework assignments: 10%
- Exams (mid-term1 and mid-term2, both in class): 20%+20%
- Machine Problems (four in total): 50%

Everyone gets three (3) days of no-questions-asked lateness, which may be divided among homework and MPs. Aside from that,

- Late MPs lose 10% a day down to 50% (one week late), after which no credit is given.
- Homework will not be accepted late.

Homework and MPs are to be the student's own work, not a collaborative or plagiarized work. However, students are permitted and encouraged to help one another by engaging in discussion of course material and approaches to solving the problems.

Assignments may be handed into the TA mailbox near the east entrance on the first floor of Everitt, or in person at lecture. Please email the TA when handing in late.