

Course Timeline

Due Date	Homework/Project/Exam
Fri. 8/31	Homework 1: representations and bits
Tues. 9/4	Lab 0: introduction to the lab environment
Fri. 9/7	Homework 2: operations on bits
Fri. 9/14	Homework 3: basic C programs
Tues. 9/18	Midterm Exam 1: 8:00 – 10:00 p.m.
Fri. 9/21	Homework 4: more representations
Fri. 9/28	Homework 5: Boolean algebra
Fri. 10/5	Homework 6: logic elements
Fri. 10/12	Homework 7: basic sequential logic
Tues. 10/16	Midterm Exam 2: 8:00 – 10:00 p.m.
Fri. 10/19	Homework 8: finite state machines
Tues. 10/23	Lab 1: combinational logic
Fri. 10/26	Homework 9: logic components
Tues. 10/30	Lab 2.1: sequential logic (checkpoint)
Fri. 11/2	Homework 10: logic design with components
Tues. 11/6	Lab 2.2: sequential logic
Fri. 11/9	Homework 11: LC-3 machine language
Tues. 11/13	Midterm Exam 3: 8:00 – 10:00 p.m.
Fri. 11/16	Homework 12: LC-3 flow chart (for Lab 3)
Tues. 11/27	Lab 3: LC-3 binary programming
Fri. 11/30	Homework 13: LC-3 encoding and ISAs
Tues. 12/4	Lab 4.1: LC-3 assembly programming
Fri. 12/7	Homework 14: the LC-3 datapath
Tues. 12/11	Lab 4.2: LC-3 assembly programming
Tues. 12/18	Final Exam: 1:30 – 4:30 p.m. (location TBD)

(The back side of this page provides more details on due date timing and policy.)

Homeworks are due in class on the dates indicated above (all Fridays). They are due at the *beginning* of lecture. We will hand out solutions sometime in the middle of class, after which homeworks will not be accepted.

Projects are due on the dates indicated above at times indicated on the project descriptions (usually at 10 p.m. on Wednesdays). We will use automated hand-in script for submitting your project. More details later.

Examination conflicts (if any) must be reported no less than one week before the exam in order to be accommodated. Please let one of the professors know as soon as you know about them, however.

Late policy: We will not accept late homeworks. Late projects lose two points (of 100 total points) per hour late or fraction thereof.