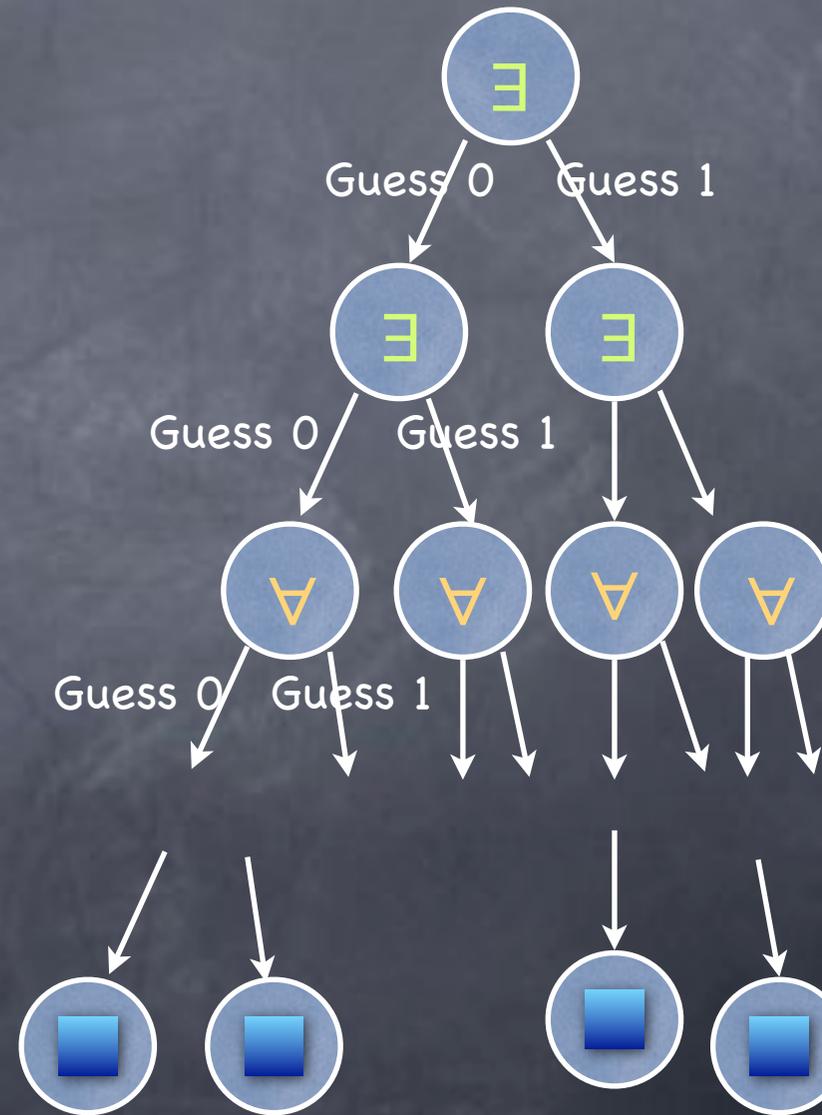


# Computational Complexity

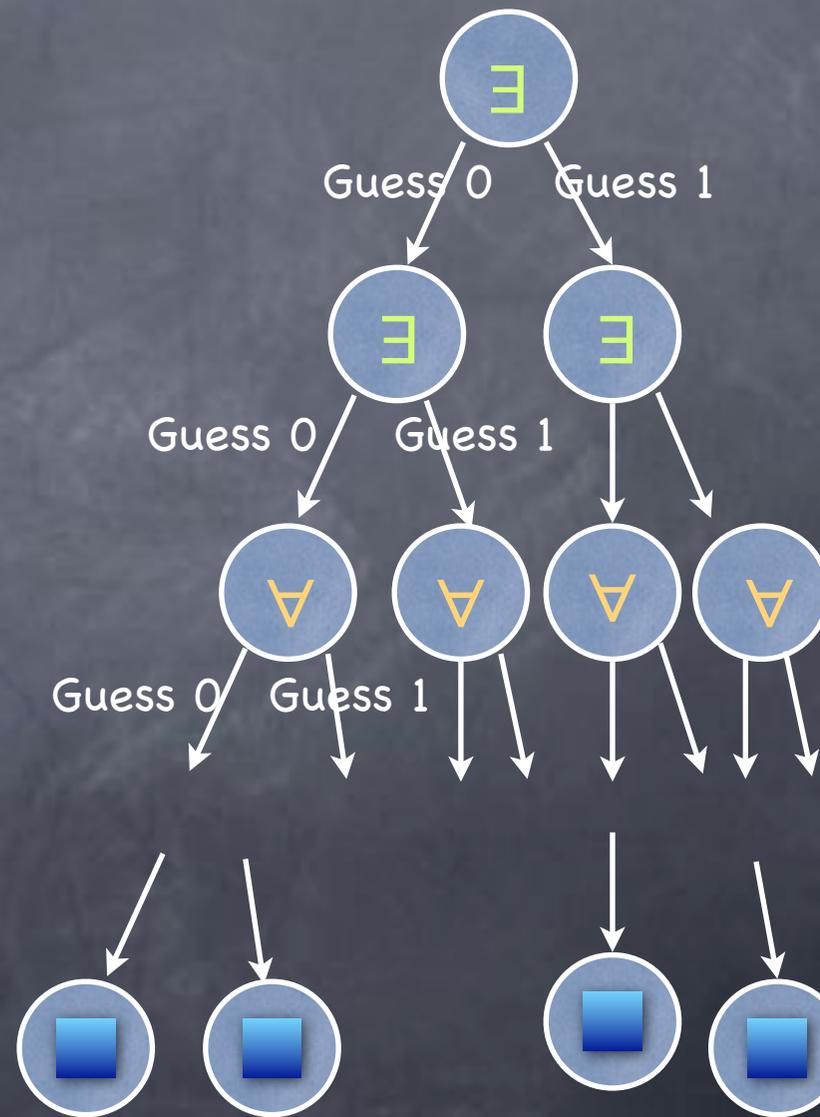
Lecture 9  
Alternation  
(Continued)

# ATM



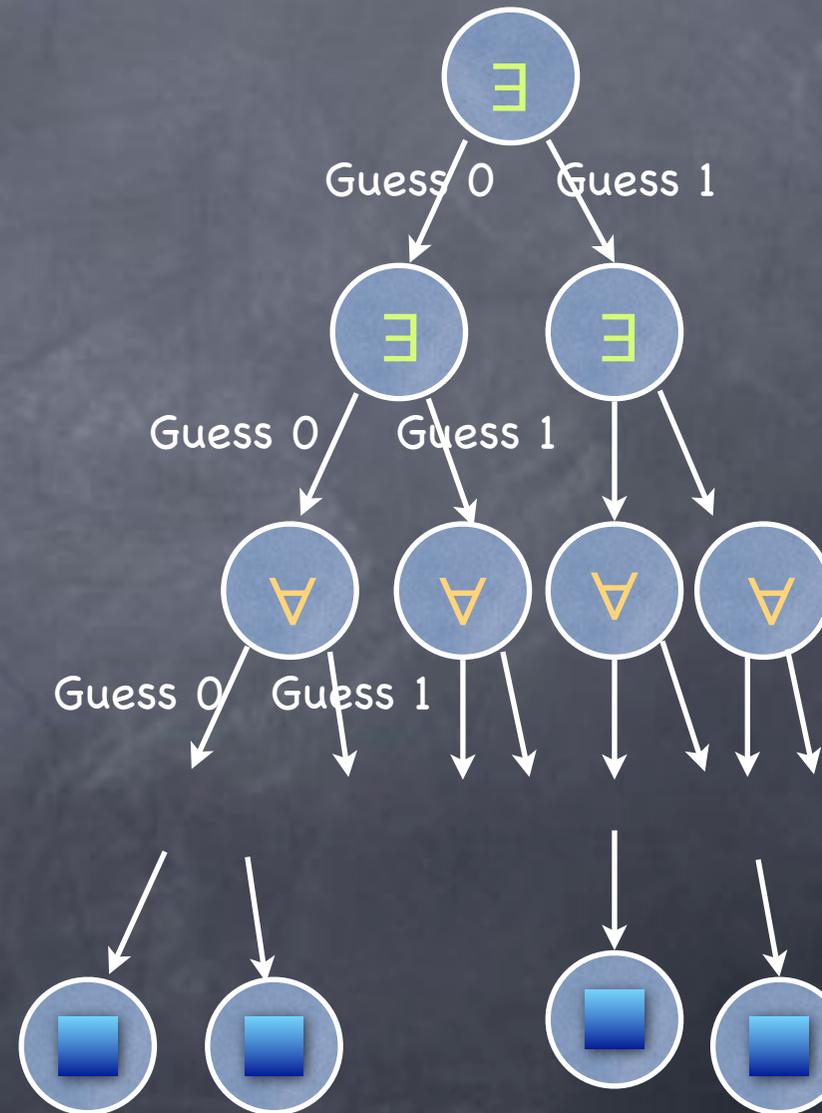
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- Alternating Turing Machine



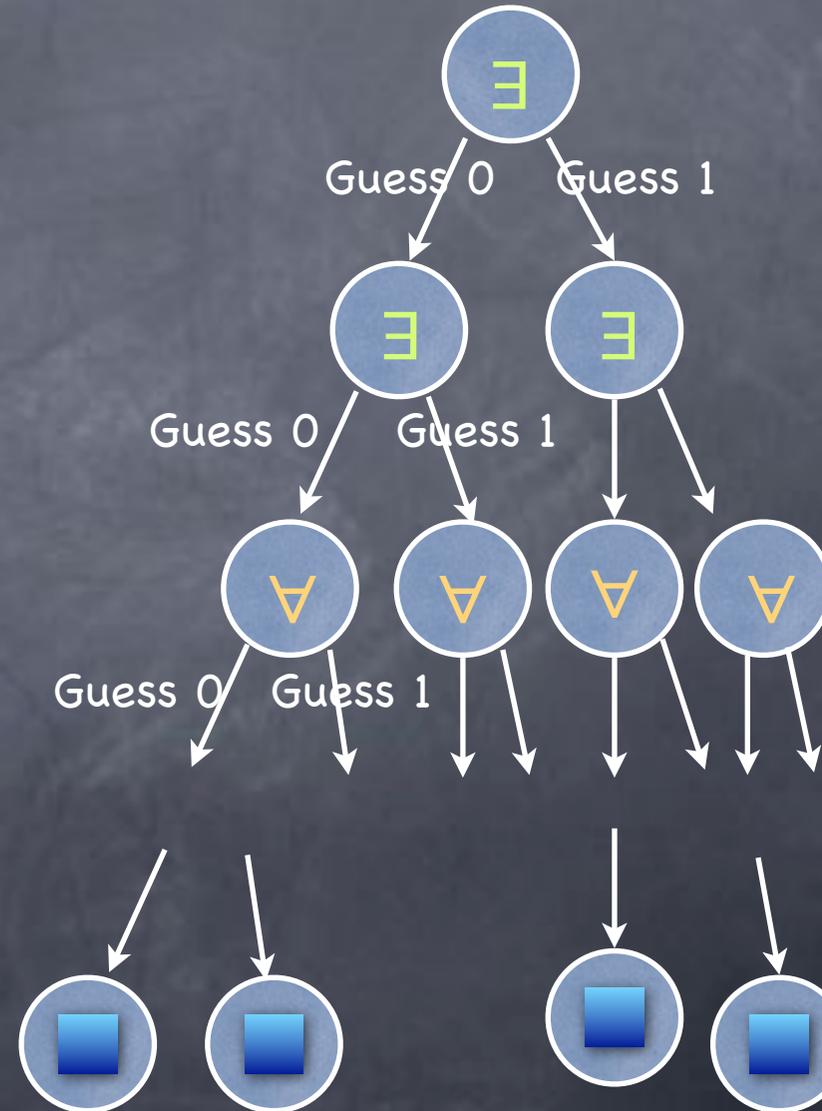
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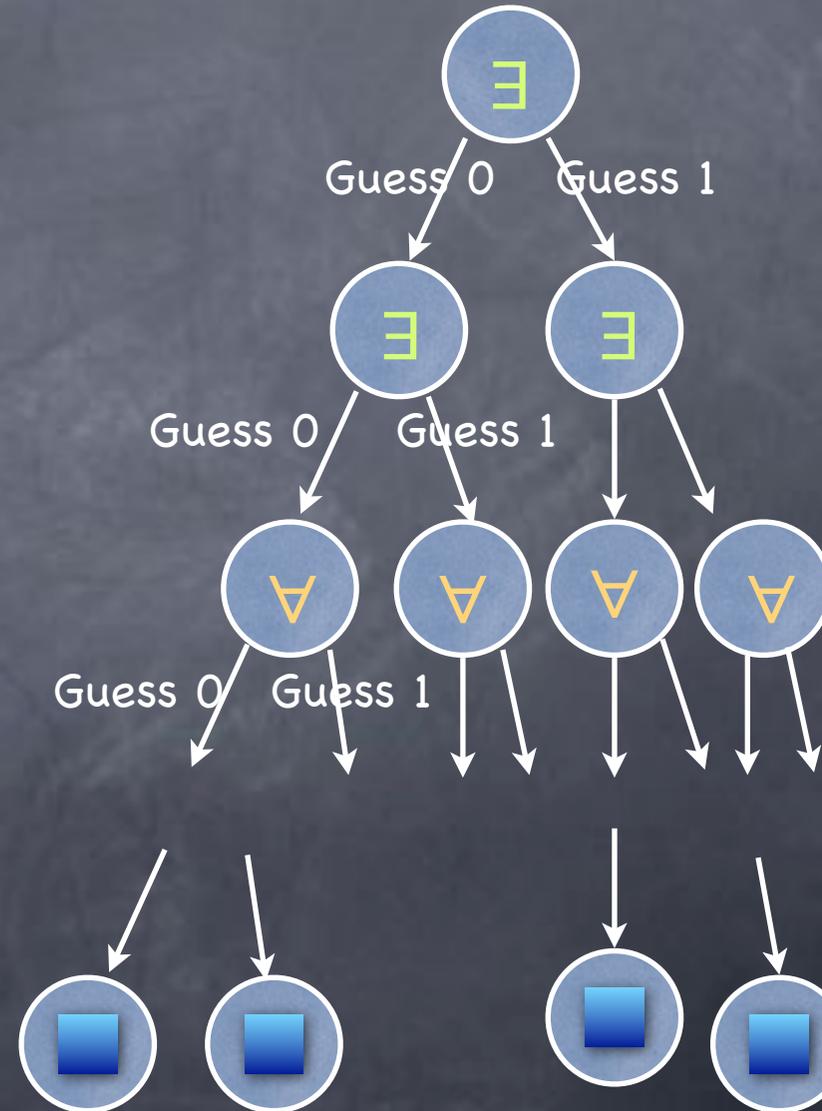
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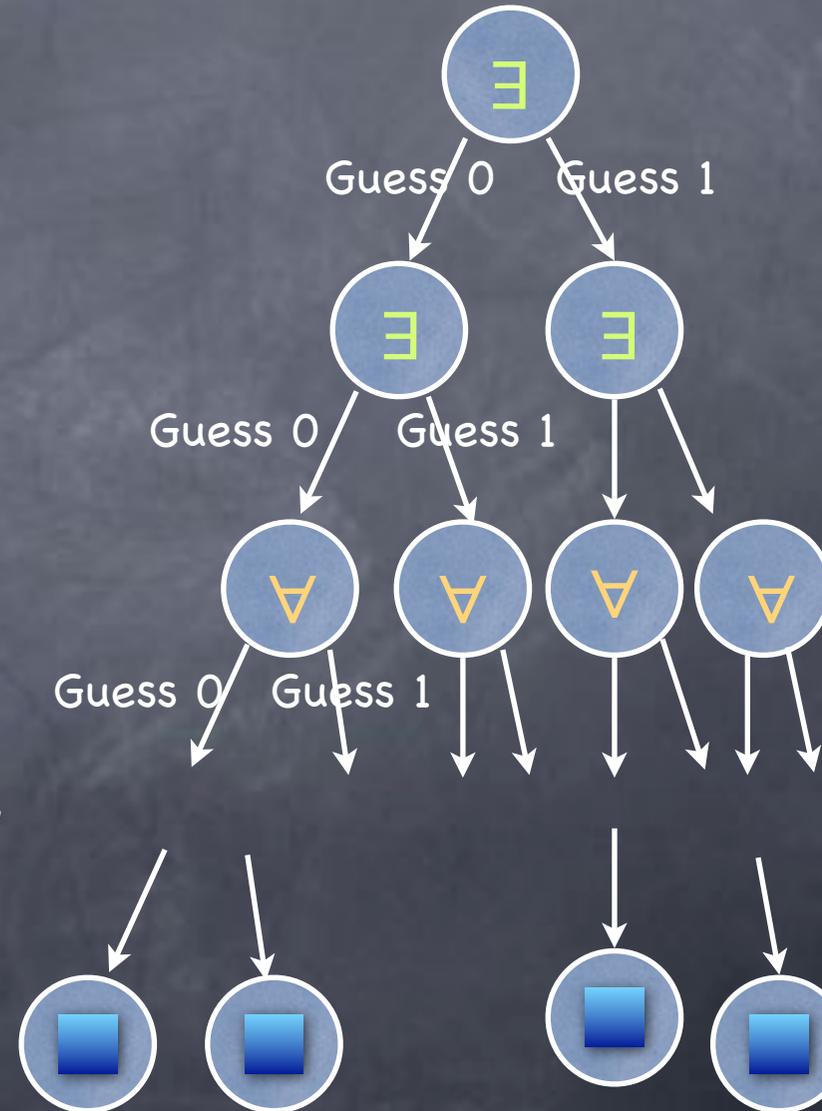
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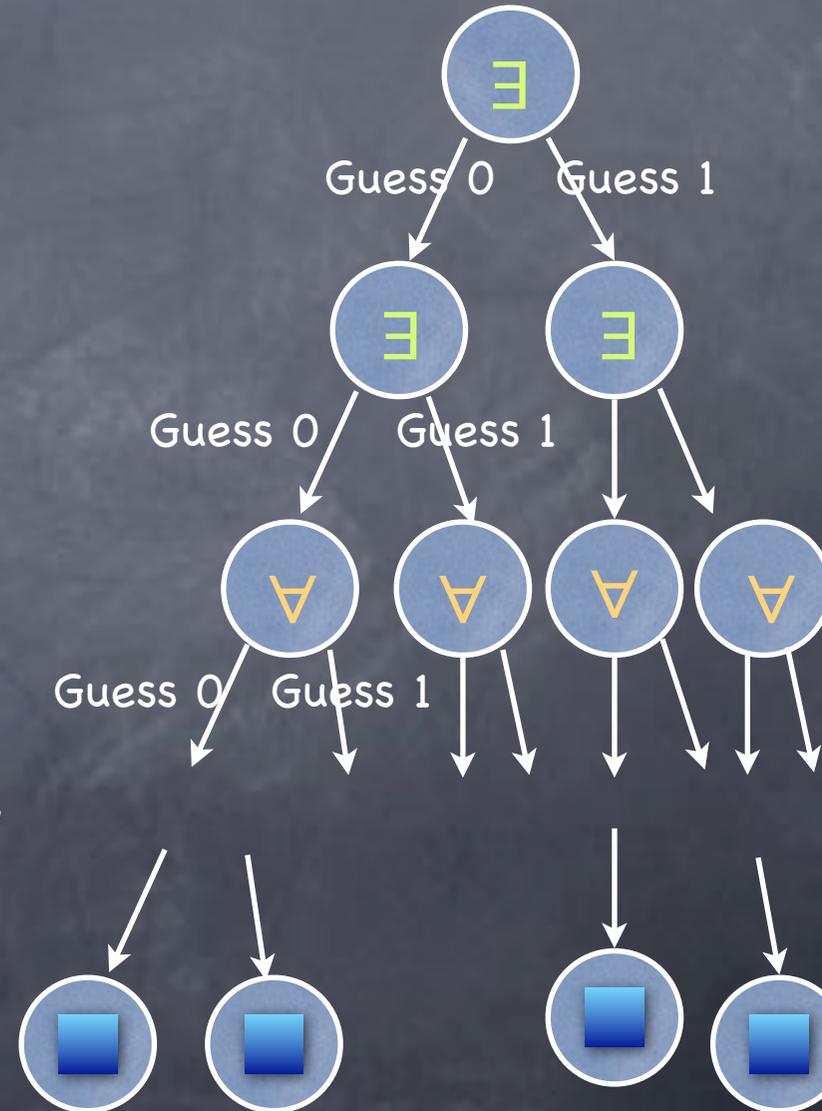
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  - $AL = P$  and  $APSPACE = EXP$

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    - Naive recursion: Extra  $O(S)$  space to store  $i,j$  at each level for  $2^{O(S)}$  levels!

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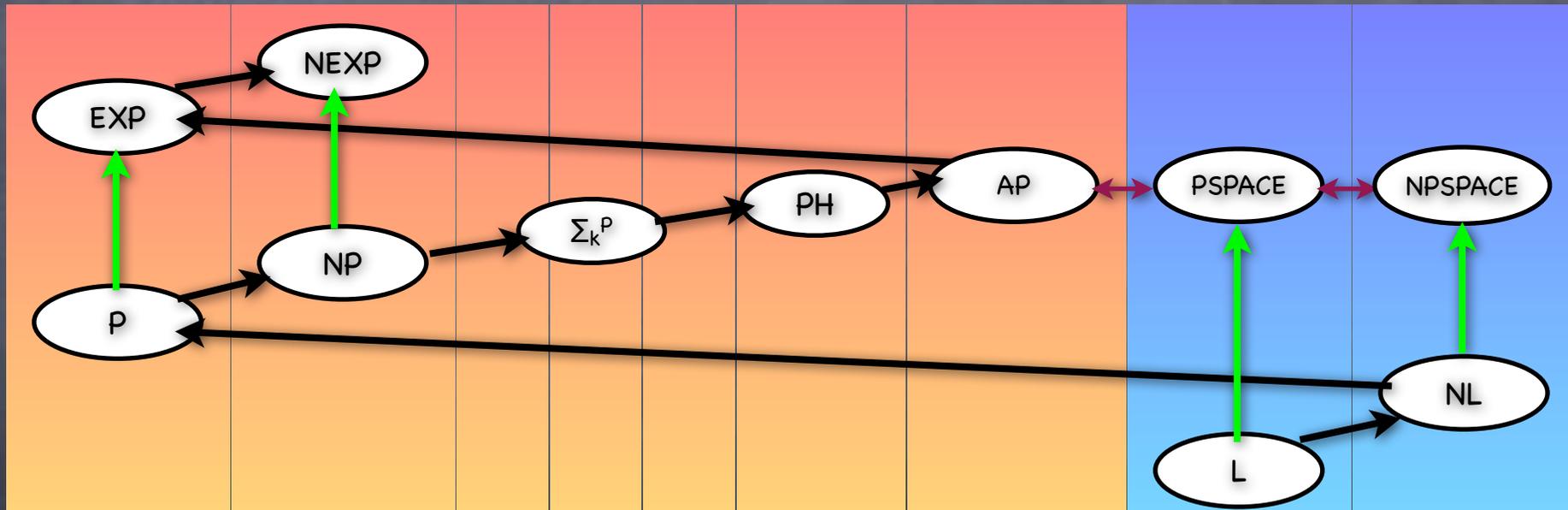
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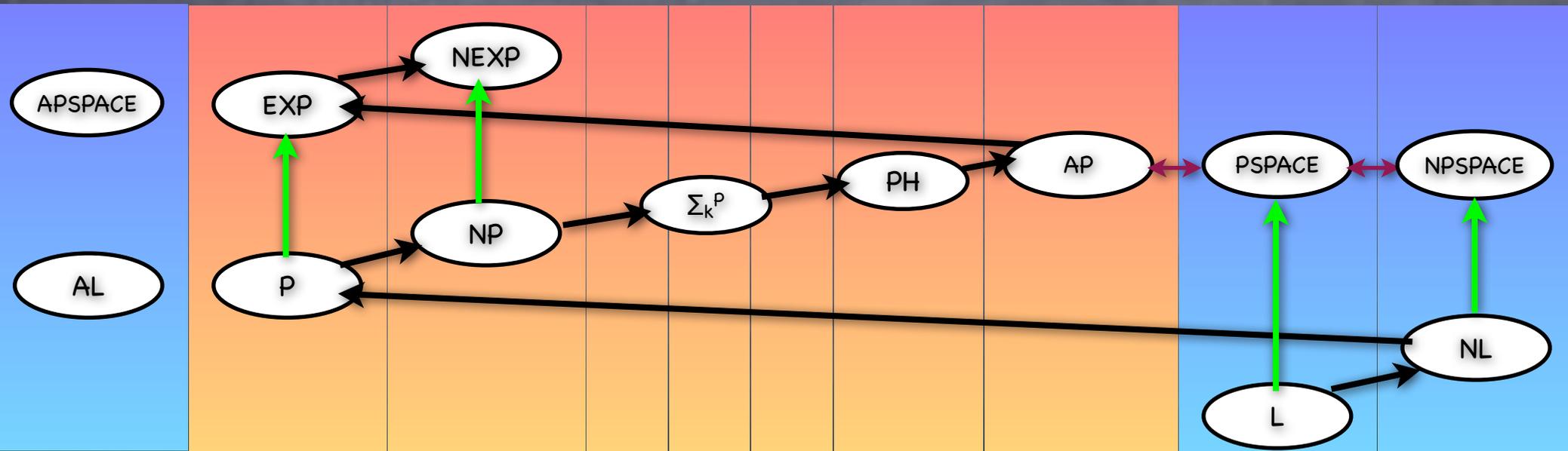
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- Stay within the same  $O(S)$  space at each level!

Gets the AND check for free. No need to use a stack.

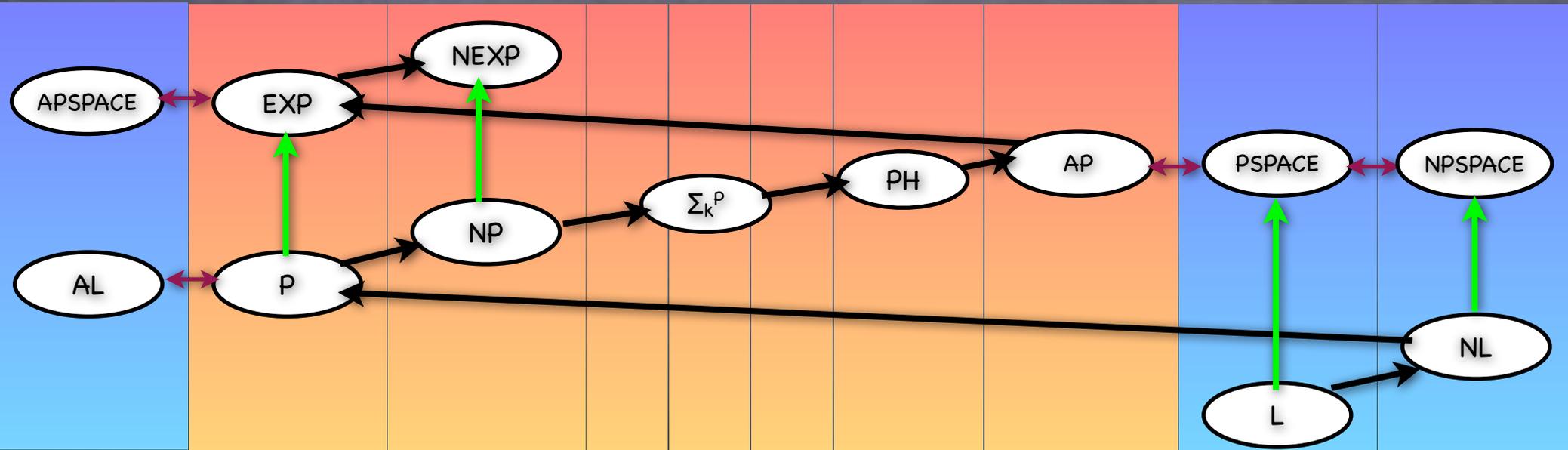
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# Non-Uniform Computation

Lecture 10

Non-Uniform Computational Models:  
Circuits

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- Non-uniform: A different “program” for each input size
  - Then complexity of building the program and executing the program
  - Sometimes will focus on the latter alone
  - Not entirely realistic if the program family is uncomputable or very complex to compute

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    - e.g. advice to decide undecidable unary languages

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NP vs.  $P/\log$ ,  $P/\text{poly}$

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  - Does P/log or P/poly contain NP?

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Recall

# Search using Decision

- Suppose given “oracles” for deciding all NP languages, can we easily find certificates?
  - Yes! So, if decision easy (decision-oracles realizable), then search is easy too!
- Say, given  $x$ , need to find  $w$  s.t.  $(x,w) \in L'$  (if such  $w$  exists)
  - consider  $L_1$  in NP:  $(x,y) \in L_1$  iff  $\exists z$  s.t.  $(x,yz) \in L'$ . (i.e., can  $y$  be a prefix of a certificate for  $x$ ).
  - Query  $L_1$ -oracle with  $(x,0)$  and  $(x,1)$ . If  $\exists w$ , one of the two must be positive: say  $(x,0) \in L_1$ ; then first bit of  $w$  be 0.
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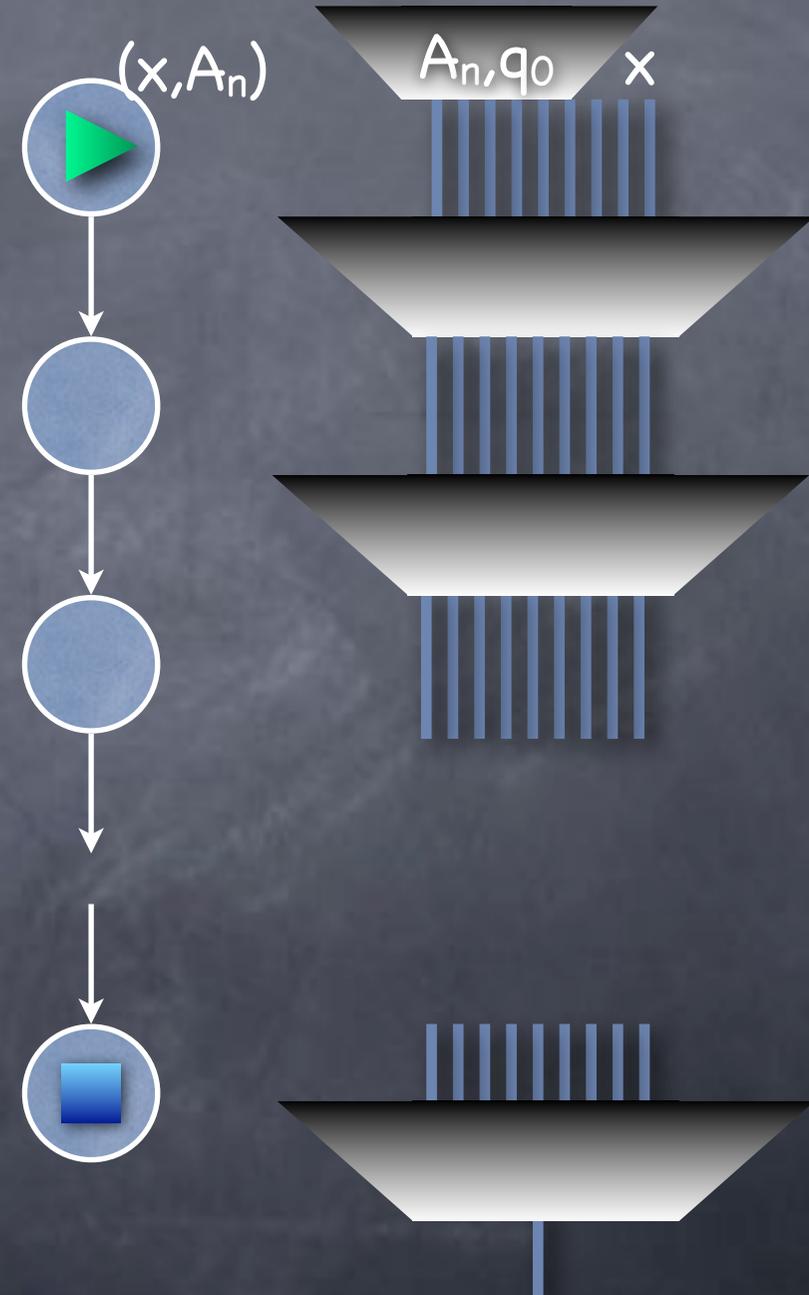
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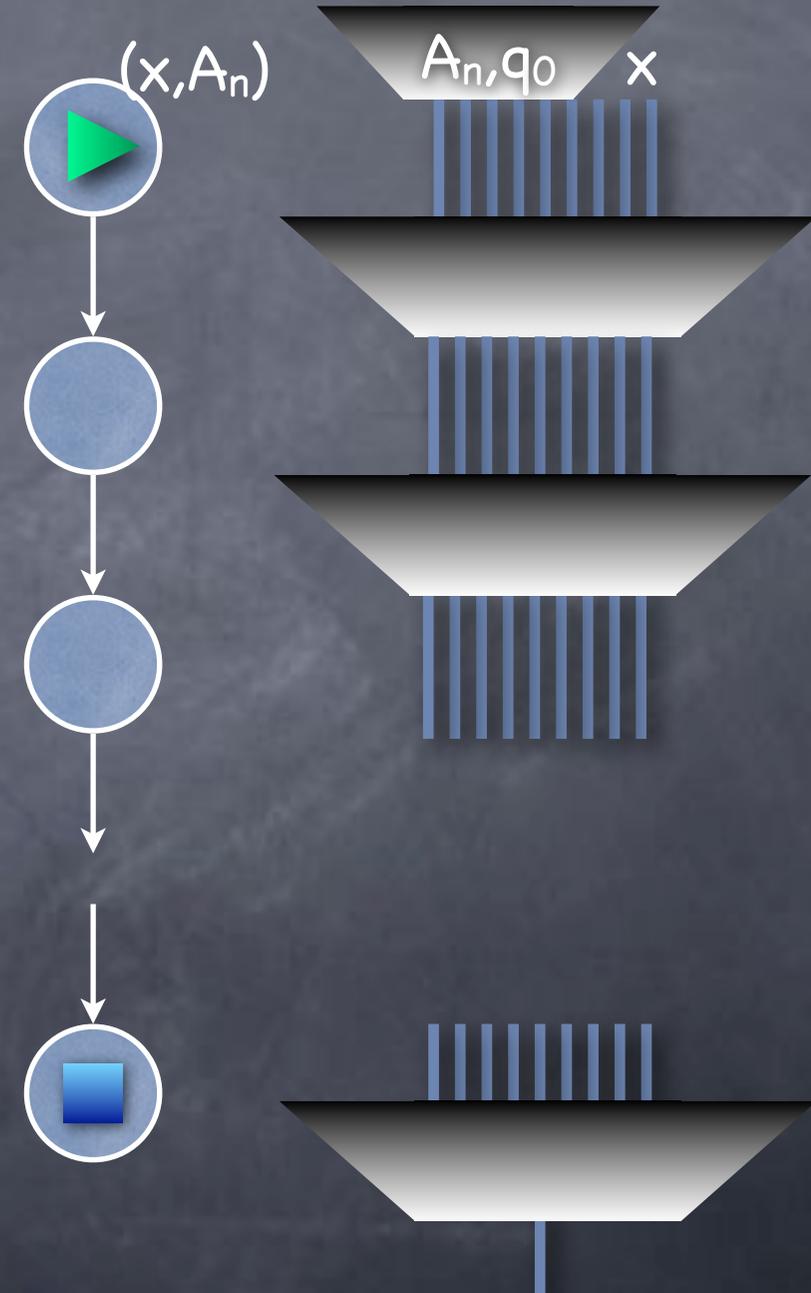
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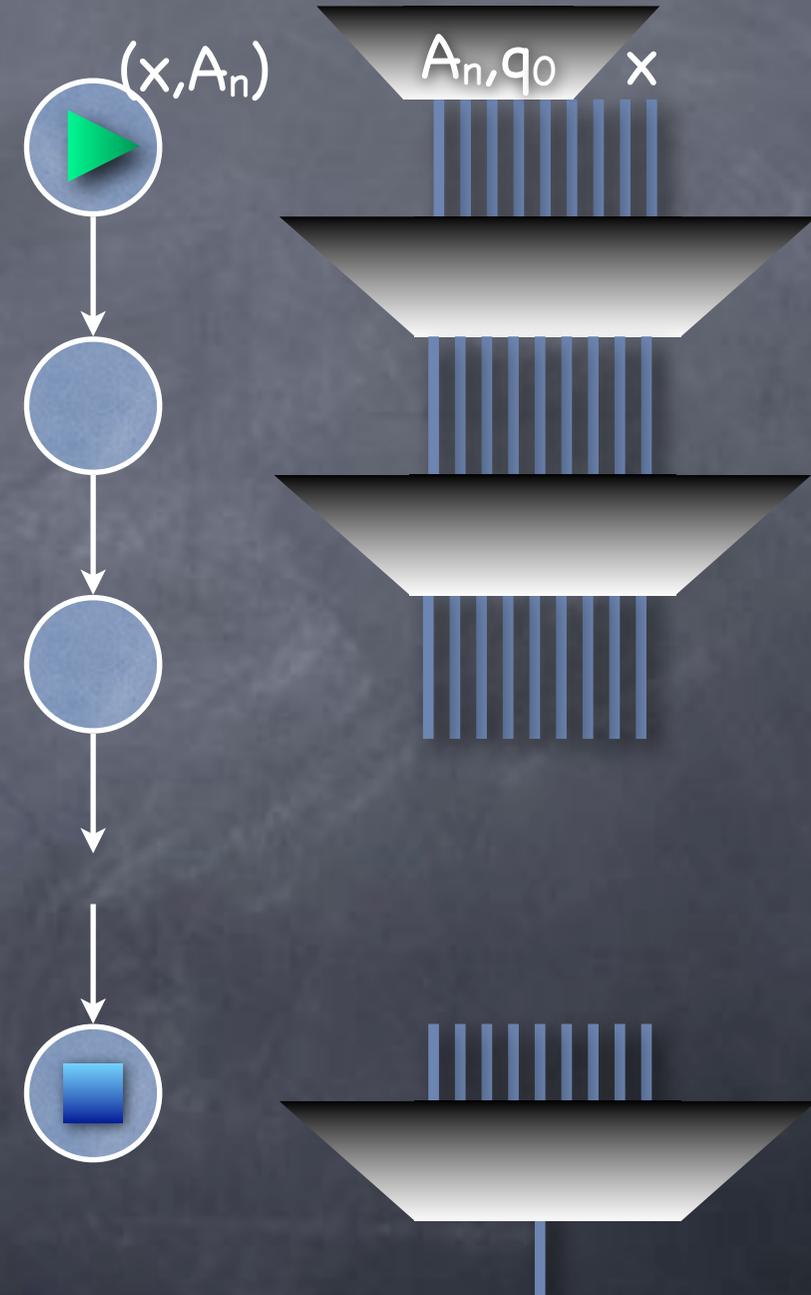
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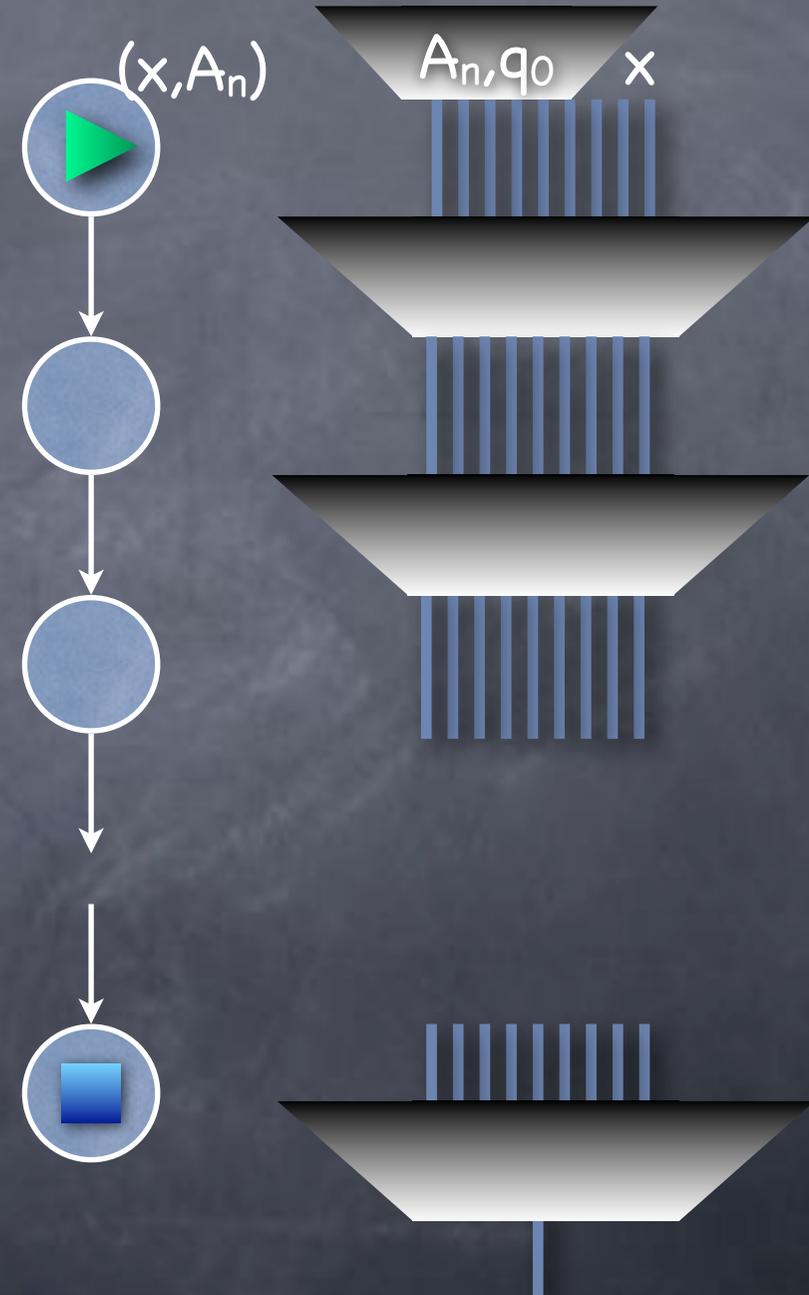
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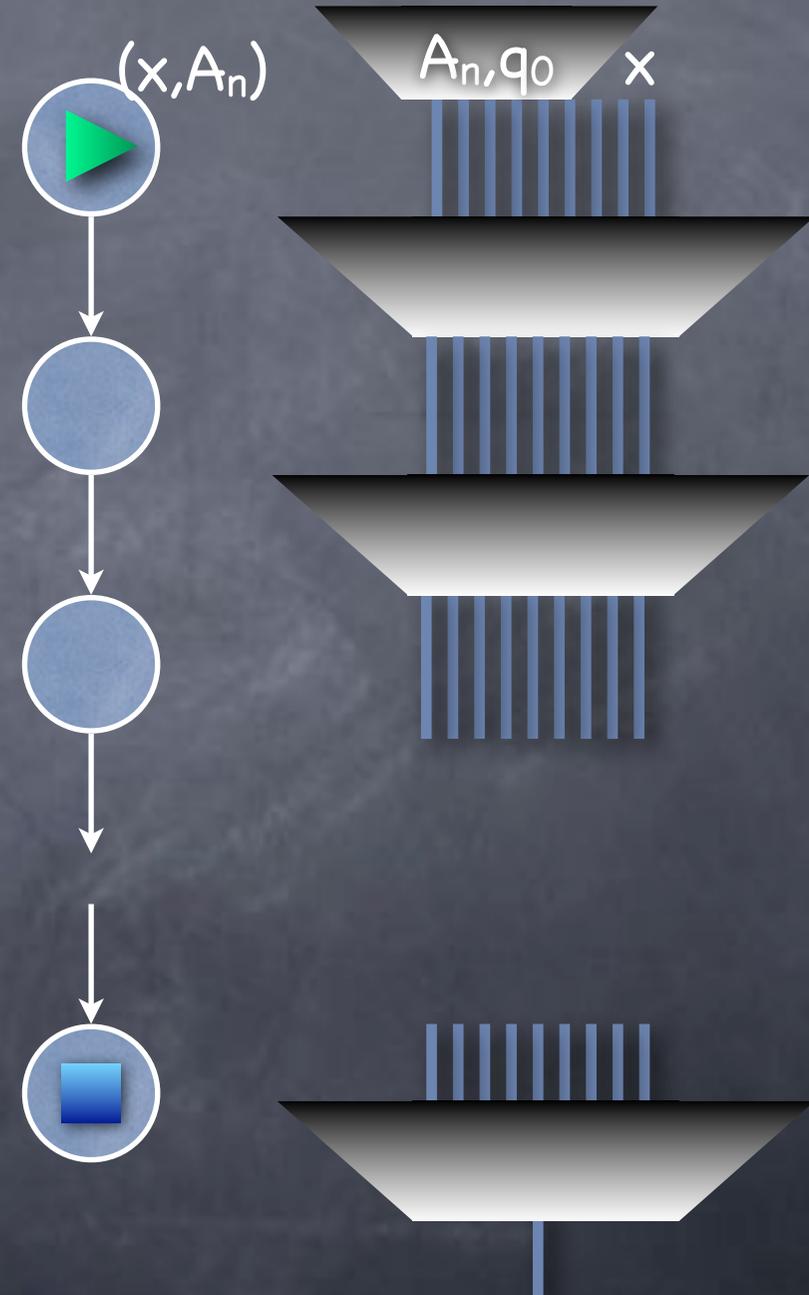
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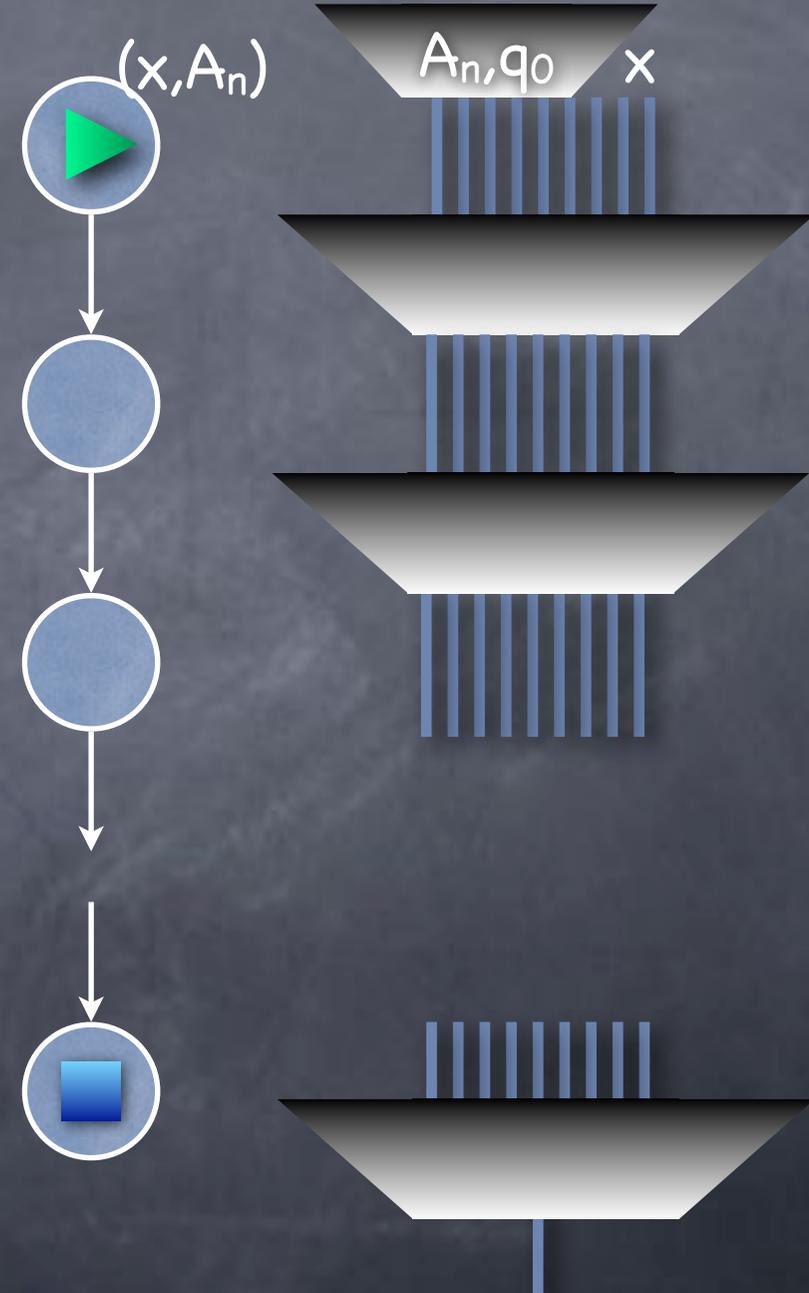
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  - An  $O(\log n)$  space TM can compute the circuit

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- Open problem: Is  $NC = P$ ?

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